

Test Readiness Review



Nano-Stratospheric Aerosol Measurement



Industry Sponsor: Ball Aerospace Industry Advisors: Jaykob Velasquez, Jim Baer, Patrick Wessels Faculty Advisor: Matt Rhode

Team CU-LATR

Gerald Yoho Lucio Murillo Jade Babcock-Chi Lucca Trapani David Hightower Abdoulaye Diallo Garrett Bell Benji Brandenburger CJ Kennedy 3/1/2023 11:30 - 12:20



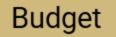




Project Overview

Project Overview

Schedule



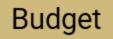


The NanoSAM-IV Mission Statement

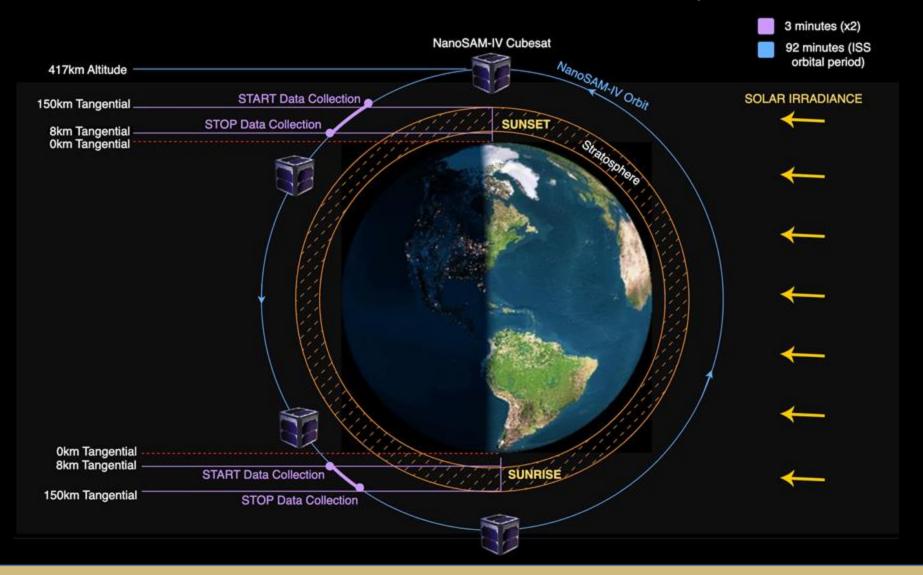


The NanoSAM-IV team will create a laboratory simulation of the Ball Aerospace SAM program missions focusing specifically on collecting and processing simulated solar irradiance data at zero aerosol attenuation (full signal) in an automated fashion.

Schedule



NanoSAM Mission ConOps



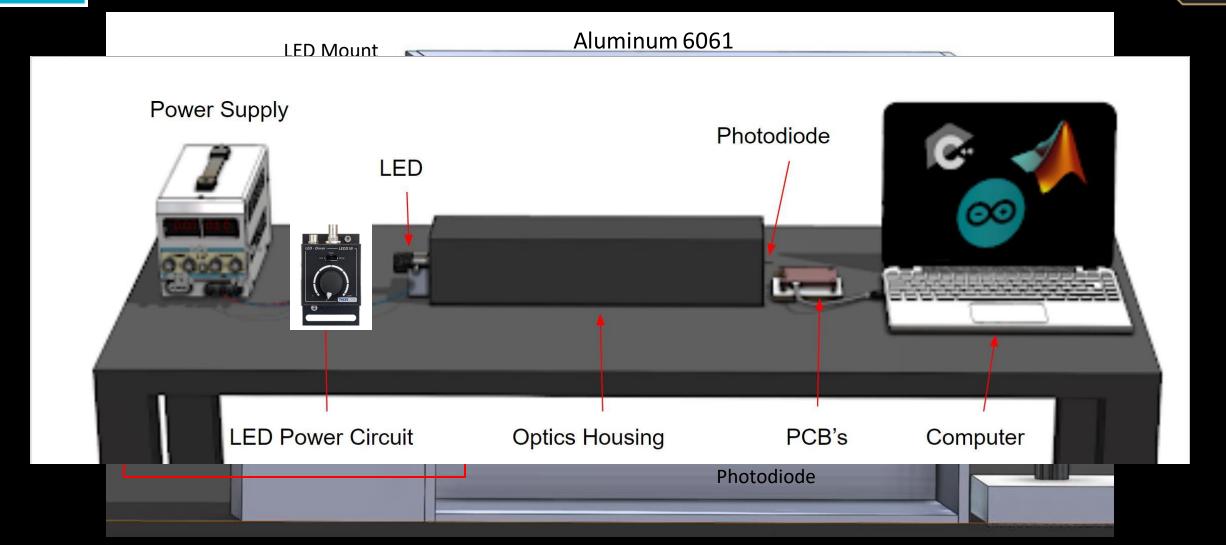
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The NanoSAM-IV Design



Project Overview

Schedule

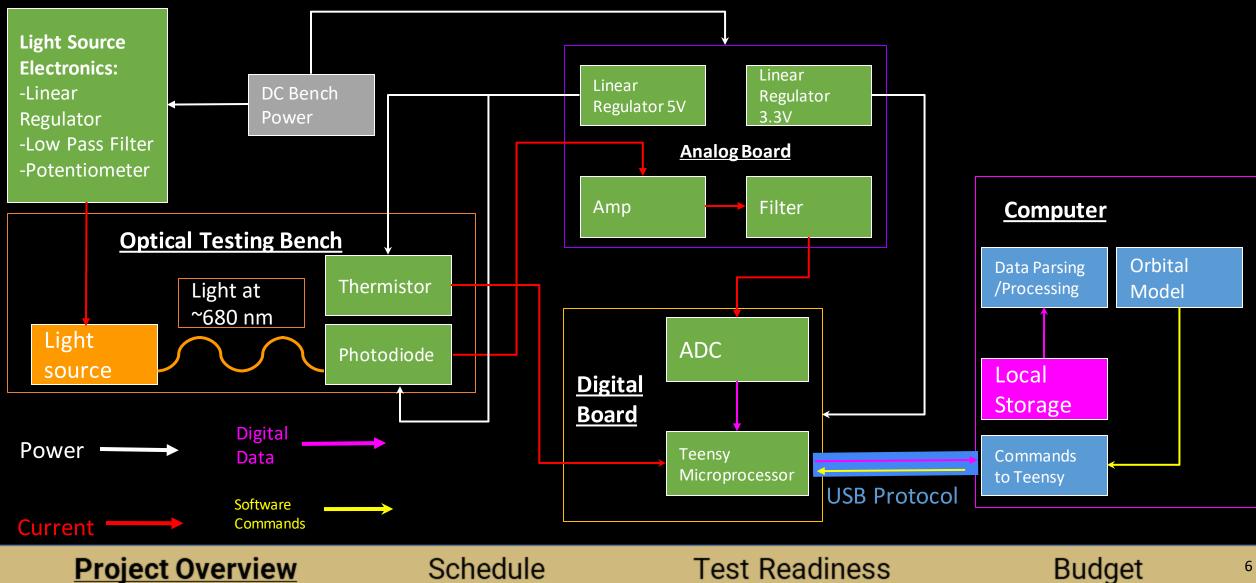
Test Readiness

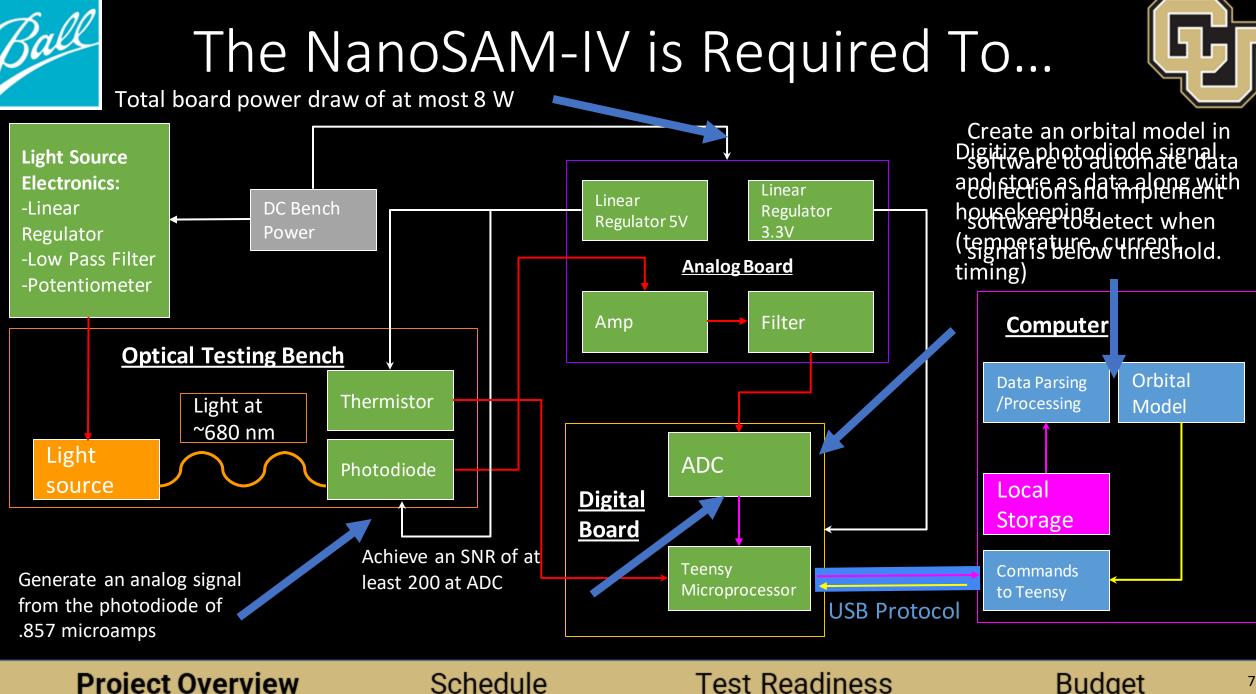
Budget

The NanoSAM-IV FBD

Ball



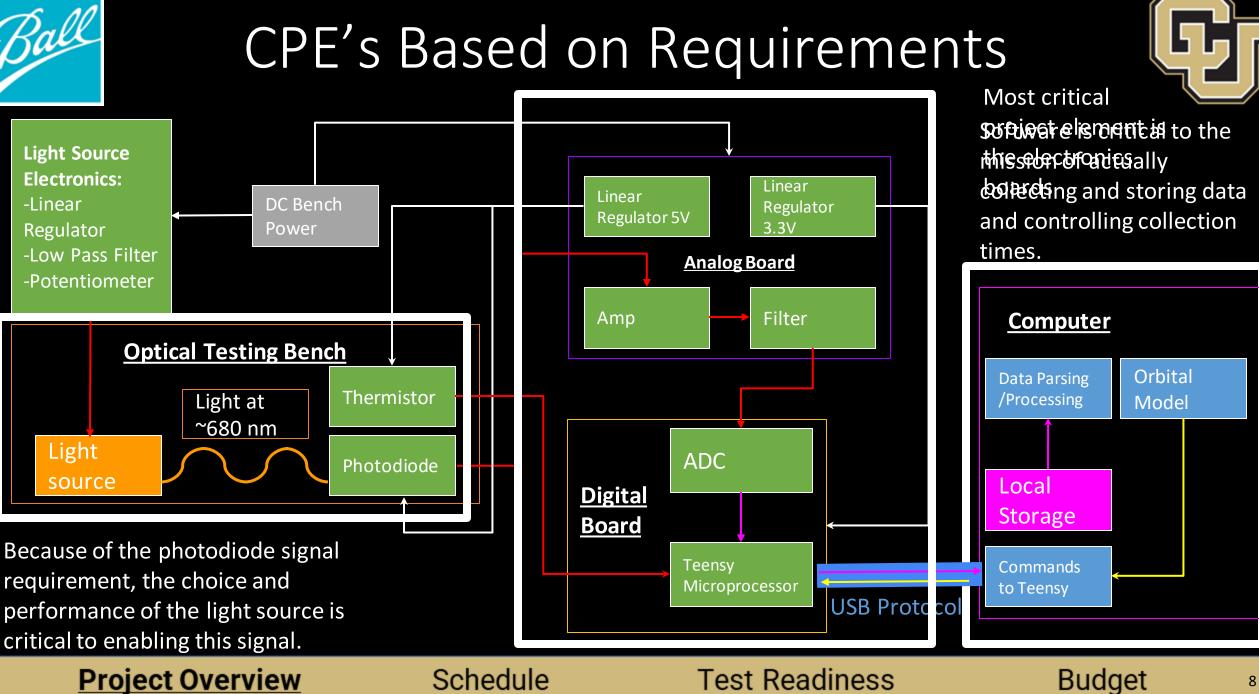




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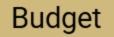




Schedule

Project Overview

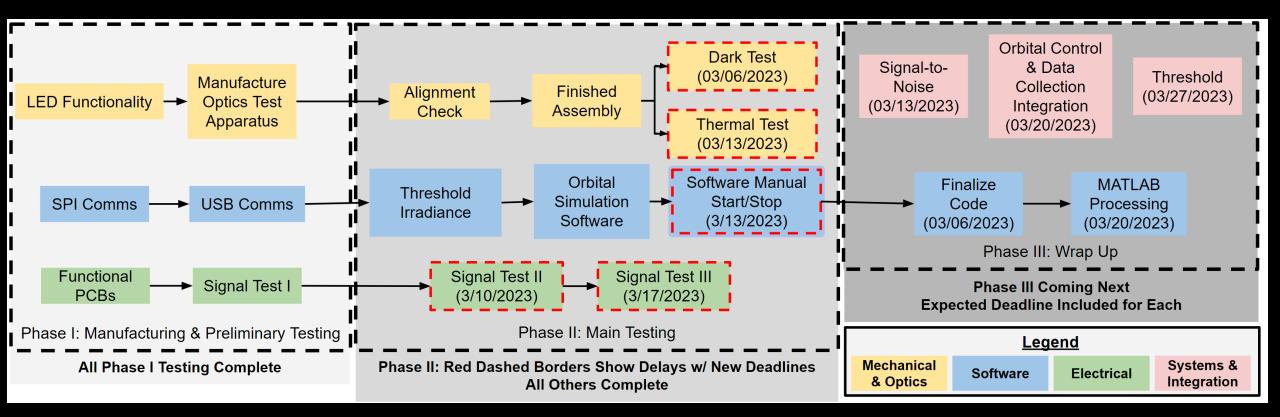
Schedule





Schedule Overview





Project Overview

Schedule

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Changes to Schedule (Big Picture)



			Task Scheduled																			
	Complete		Built In Margin																			
Legend (status)	In Progress Delayed	Legend	Old Critical Path									We Are Here	1									
			New Critical Path									ne Ale Here										
	L					2023 January February March April Ma												Мау				
					Total Days		January		· · · · ·	rebluary				Warch			1		Арпі			way
	Task	Start Date	End Date	Buffer/Margin [+ days]	(including margin)	Week 1	Week 2	Week 3	Week 4	Week 5	Week 6	Week 7	Week 8	Week 9	Week 10	Week 11	Week 12	Week 13	Week 14	Week 15	Week 16	Week 17
Status	Manufacturing/Preliminary Testing																					
complete	LED Functionality Check	1/17/2023	1/31/2023	3	18																	
complete	Manufacture Box/Optics Apparatus	1/17/2023	1/31/2023	3	18																	
complete	SPI Comms Test	1/17/2023	1/31/2023	3	18																	
complete	USB Comms Test	1/17/2023	1/31/2023	3	18																	
complete	Signal Test I	1/17/2023	1/31/2023	3	18																	
complete	AIAA Abstract	1/25/2023	2/2/2023	1	10			D U E				٤										
complete	Internal Design Review (IDR)	1/22/2023	2/7/2023	3	20				DUE			Ð										
	Main Testing											ല										
complete	Threshold Irradiance Check	2/1/2023	2/11/2023	3	14							2										
complete	Alignment Check	2/1/2023	2/11/2023	3	14							D D										
complete	Orbital Simulation Software Check	2/1/2023	2/11/2023	3	14							_										
in progress	Signal Test II	2/1/2023	2/11/2023	3	14							Ъ										
in progress	"Stress Test"	2/12/2023	2/22/2023	3	14							D D										
in progress	Finished Assembly	2/12/2023	2/22/2023	3	14							5										
delayed	Signal Test III	2/12/2023	2/22/2023	3	14							Ö.										
delayed	"Dark" Test	2/19/2023	2/25/2023	2	9																	
delayed	Thermal Test	2/19/2023	2/25/2023	2	9						<u> </u>											
in progress	Test Readiness Review (TRR)	2/10/2023	2/21/2023	7	19							D U E										
in progress	AIAA Paper	2/3/2023	2/26/2023	17	41								D U E									
	Final Wrap-Up Phase																					
	Finalized Code	2/6/2023	3/6/2023	3	32																	
	Signal to Noise Test	3/1/2023	3/13/2023	7	20																	
	MATLAB Processing Check	3/6/2023	3/20/2023	3	18																	
	Orbital Control and Data Collection (OCDC) Integration Test	3/13/2023	3/20/2023	3	11																	
	Threshold Test	3/20/2023	3/27/2023	3	11																	
	Symposium Poster & Presentation	3/24/2023	4/17/2023	1	26														D U E			
	Spring Final Review (SFR)	3/24/2023	4/16/2023	8	32															D U E		
	Project Final Report (PFR)	4/15/2023	4/30/2023	8	24																	D U E

Project Overview

Schedule

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Changes to Schedule (Phase I)



		Task Scheduled														
	Logond	Built In Margin														
	Legend	Old Critical Path														
		New Critical Path							2	2023						
								January					February			
	T	ask		Sub-teams	End Date	1/15 1/16	1/17 1/18 1/19 1/20 1/21	1/22 1/23 1	/24 1/25 1/26 1/27 1/28	1/29 1/30 1/31	2/1 2/2 2/3	3 2/4	2/5 2/6	2/7 2/8	2/9 2/10 2/	11
Status		Win	nter Br	eak												
Status	Manufa	acturing/Prelin	minar	y Testing												
complete		unctionality C		I /ODUCS												
complete	Manu	facture Box/O Appa	ptics ratus	Mechanical /Optics	1/31/2023											
complete		SPI Comms	s Test	Software	1/31/2023											
complete		USB Comms	s Test	Software	1/31/2023											
complete		Signal	Test I	Electronics	1/31/2023											
complete		AIAA Abs	stract	All	2/2/2023						DRA	FT			D	JE!
complete	Inter	rnal Design Re	eview (IDR)		2/7/2023										DUE	

Project Overview

Schedule

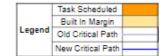
Test Readiness

Budget



Changes to Schedule (Phase II)





	-		3223																				
							Janary					Feb	naky								March		
		Task	Sub-teams	End Date	Margin	Total Days (including margin)	122 123 134 133 133 137 13	5 1,27 1,30	191 21 22 23 24	28 28 27	26 29 210 211	212 213 214	215 216 217 218	219 220 221	202 203 204 203	225 227	(25 2/5 2/	213 214	35 35	37 38 3	(P 3/10 3/11	2/12 2/13 2/14	2/15 2/16 2/17 2/15
	Status	Main Testing																					
	complete	Threshold Irradiance Check	Software	2/11/2023	3	14																	
	complete	Alignment Check	Mechanical/ Optics	2/11/2023	3	14																	
	complete	Orbital Simulation Check	Software	2/11/2023	3	14					ŕ						<						
i	n progress	Signal Test II	Electronics	2/11/2023	3	14											We						
i	n progress	"Stress Test"	Software	2/22/2023	3	14								N			a						
	complete	Finished Assembly	Mechanical/ Optics	2/22/2023	3	14								$ \rightarrow $			are h						
	delayed	Signal Test III	Electronics	2/22/2023	3	14											here						
	delayed	"Dark" Test	Mechanical/ Optics	2/25/2023	2	9											ß						
	delayed	Thermal Test	Mechanical/ Optics	2/25/2023	2	9																	
i	n progress	Test Readiness Review (TRR)	All	2/21/2023	7	19										ouer							
i	n progress	AIAA Paper	All	2/26/2023	17	41													fat DRA FT DUE				FINA L DRA FT DUE

Project Overview

Schedule

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Phase III: How We'll Stay On Schedule



	Legend Critical Path																	
											2622							
	Febuary Narch													April			May	
			e End Date	228 227 22		4 25 26	27 28 29 210 21	1 20 20 20 20 20 20	207 212	11/ 12/ 12/ 12/ 12/ 12/ 12	228 227 228 229 230 231 41	42 43 44 45 4	5 47 45	47 410 411 412 413 414 415	423 424 425 428 427 428 427	430 3	 57 58	CER 210 211 212 213
F	Final Integration & W Phase																	
	Finalized Code																	
	Signal to Noise Test																	
	MATLAB Processing		3/20/2023															
	Integration Test	All																
	Threshold Test		3/27/2023															
	Symposium Poster & Presentation	~	4/17/2023															
	Spring Final Review (SFR)	-	4/16/2023												0LP			
	Project Final Report (PFR)	All	4/30/2023														DUET	

- Test in parallel
- Assign more help to tasks that need extra support
- Worst case: all testing finished before Spring Break (last week of March)
 - 2 weeks to prepare for symposium

Project Overview

Schedule

Test Readiness









Test Readiness

Project Overview

Schedule

Test Readiness





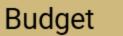
Mission Objectives



MO-01	Generate light signal that will simulate expected on-orbit, un-attenuated signal in order to test embedded system (photodiode, signal cond., ADC, microcontroller)
MO-02	Integrate embedded system design from NanoSAM II (Analog and Digital PCB's) to collect data with an SNR of 200 or greater
MO-03	Autonomous control of data collection process through timing that matches predicted orbital data collection windows
MO-04	Instrument must be appraised of when intensity of light source falls below a threshold

Project Overview

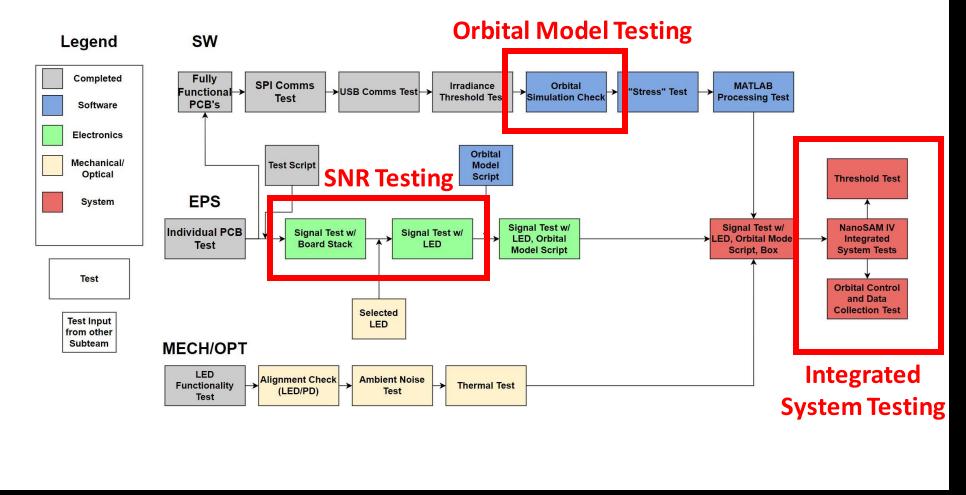
Schedule





Test Plan Overview





Project Overview

Schedule

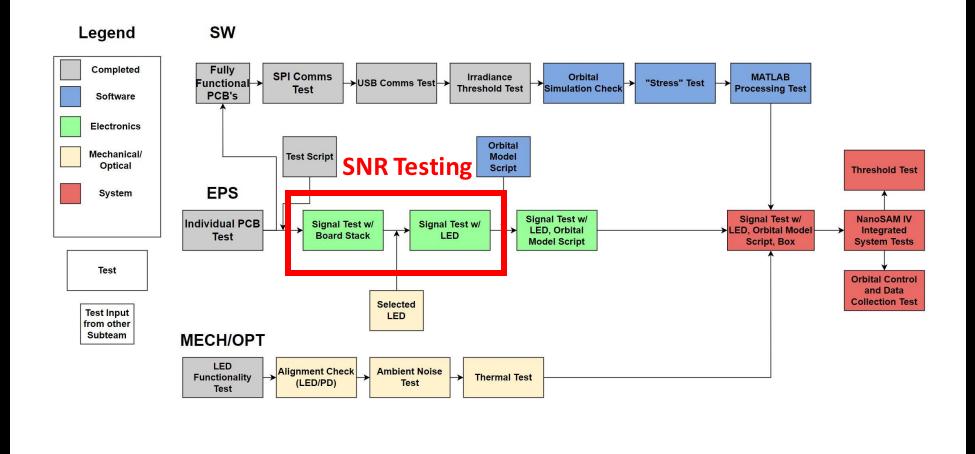
Test Readiness

Budget



Test #1 – SNR





Project Overview

Schedule

Test Readiness

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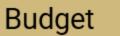
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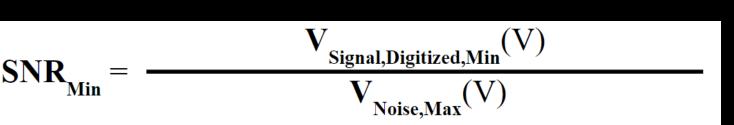
Project Overview

Schedule





Closed-Loop Model Verification



$$\mathbf{V_{Noise}} = \sqrt{\frac{V^2_{\text{Shot,PD}} + V^2_{\text{Dark,PD}} + V^2_{\text{Johnson}} + \frac{V^2_{\text{OpAmp}} + V^2_{\text{ADC}}}{2}} \xrightarrow{\text{App}}$$

$$\mathbf{V_{Noise}} = \sqrt{\frac{V_{Photodiode}^{2} + V_{OpAmp}^{2} + V_{ADC}^{2}}{\frac{V_{Photodiode}^{2} + V_{OpAmp}^{2} + V_{ADC}^{2}}{\frac{V_{Photodiode}^{2} + V_{OpAmp}^{2} + V_{ADC}^{2}}}{\frac{V_{Photodiode}^{2} + V_{OpAmp}^{2} + V_{ADC}^{2}}{\frac{V_{Photodiode}^{2} + V_{ADC}^{2}}{\frac{V_{Photodiode}^{$$

Approximation due to very low noise values from the photodiode components

Expected results:

Voltage Noise	Value [V]
$V_{photodiode}^2$	2.86e-06
V_{Op-amp}^{2}	4e-08
V _{ADC} ²	2.1e-10

Project Overview

Schedule

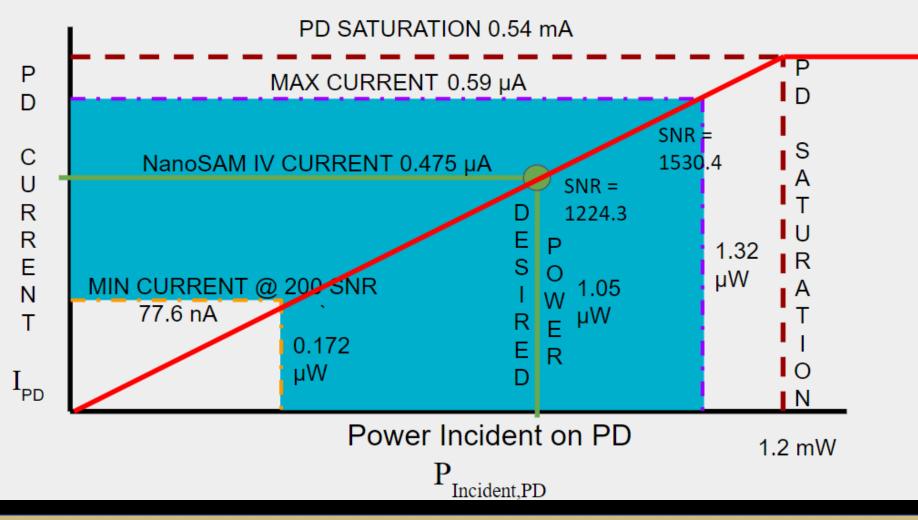
Test Readiness

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SNR Testing – Design Space & Bounds





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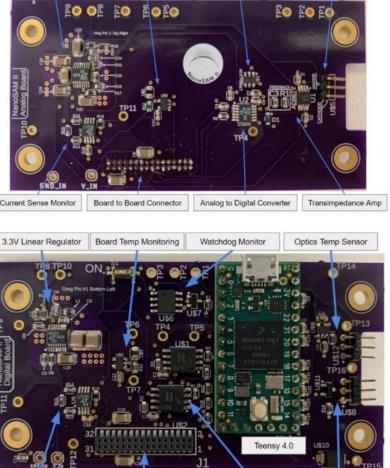


Pre-requisite test 1: Retrofitted and tested PCB's

Voltage measurements were taken at all test points of the boards and actual values matched our expected measurement values



Boards components functionality



Flash Storage (2x)

3.3V Voltage Reference

Board Temp Monitoring

Photodiode



Project Overview

Schedule

Test Readiness

Board to Board Connector

Current Sense Monitor

5V Bipolar Regulator

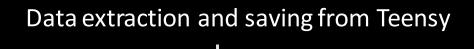


Resistor Heate





Pre-requisite test 2: Software test script





Communication line between the computer and the Teensy microcontroller



Project Overview

Schedule







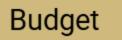
Pre-requisite test 3: Prototype optical housing

LED placement and setup



Project Overview

Schedule





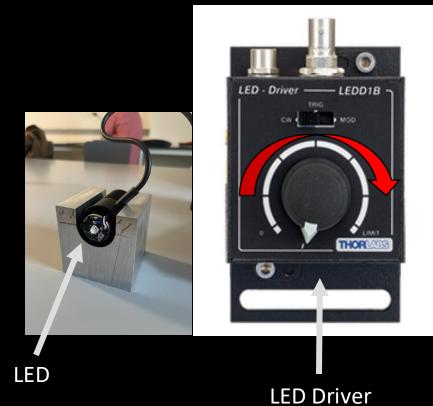


Pre-requisite 4: LED equipment

Functionality of the LED was verified and validated at desired output voltage and current

LED equipment





Project Overview

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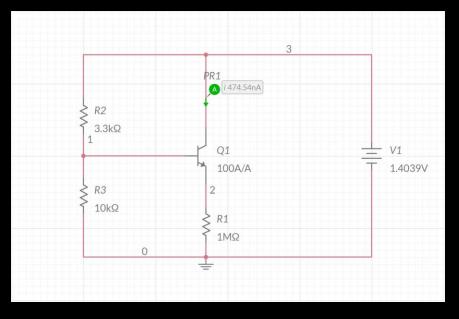


Pre-requisite test 5: Mock photodiode circuit



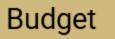
Mock photodiode circuit

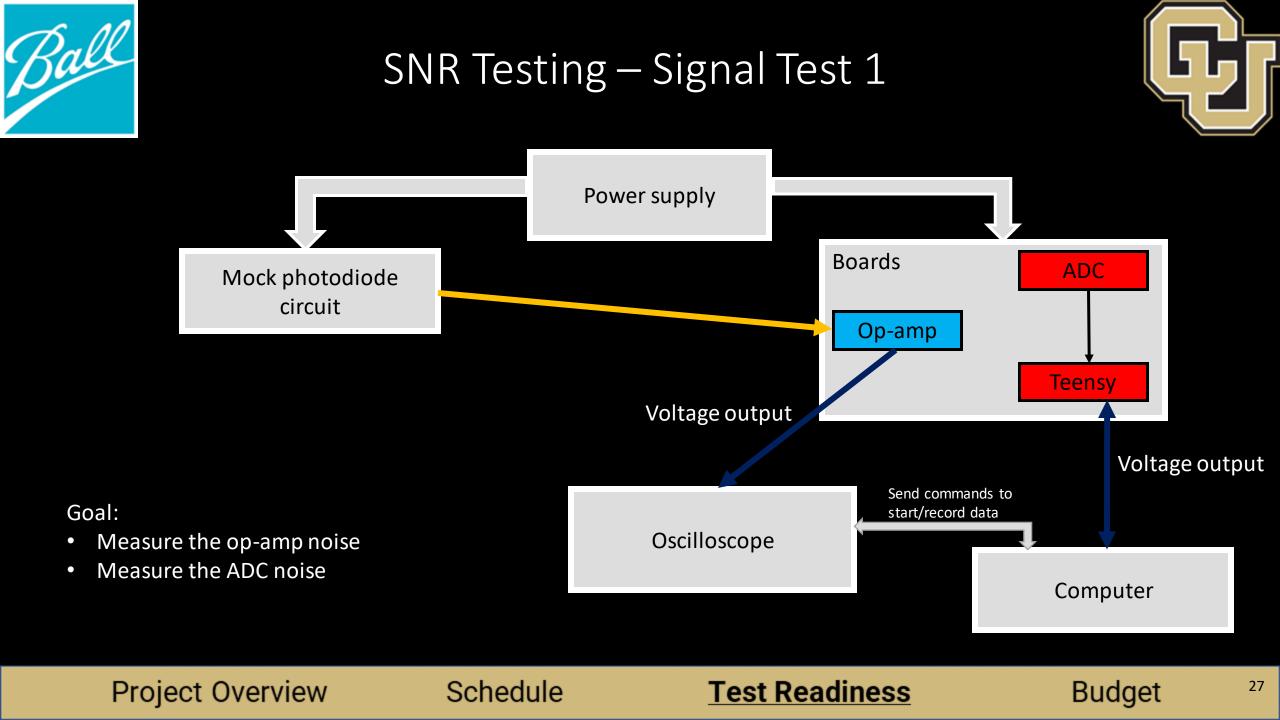


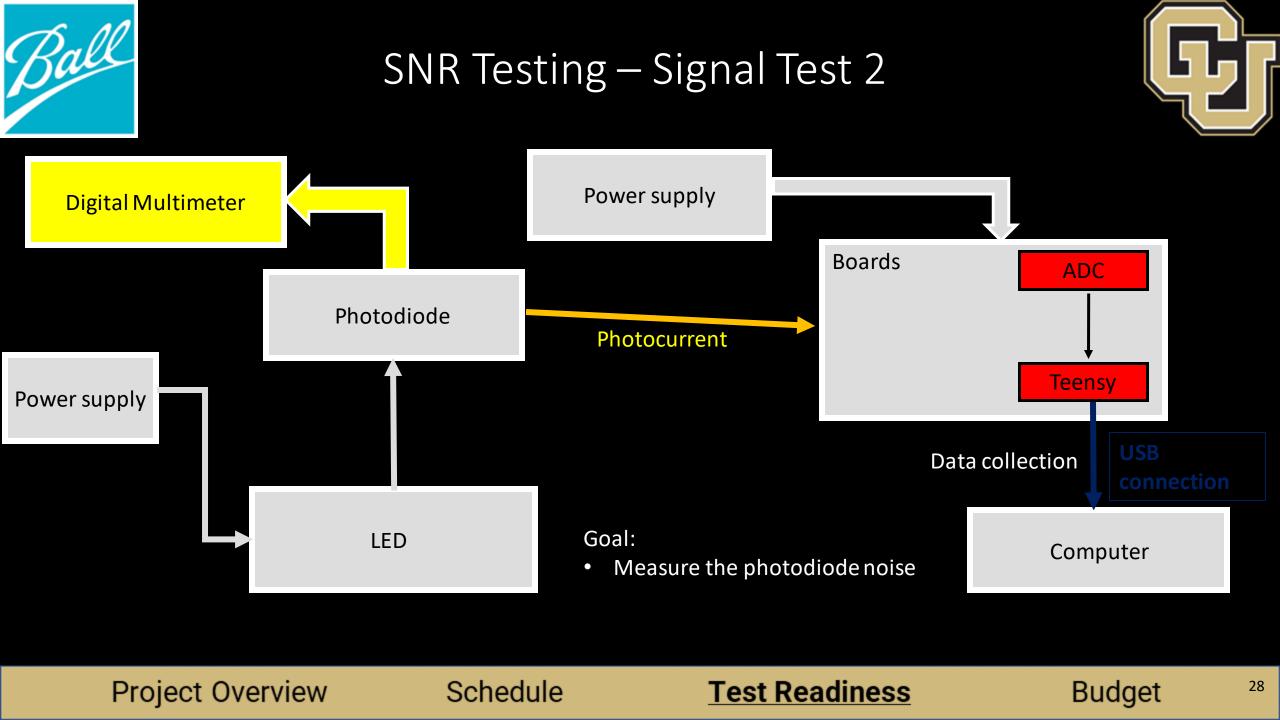


Project Overview

Schedule



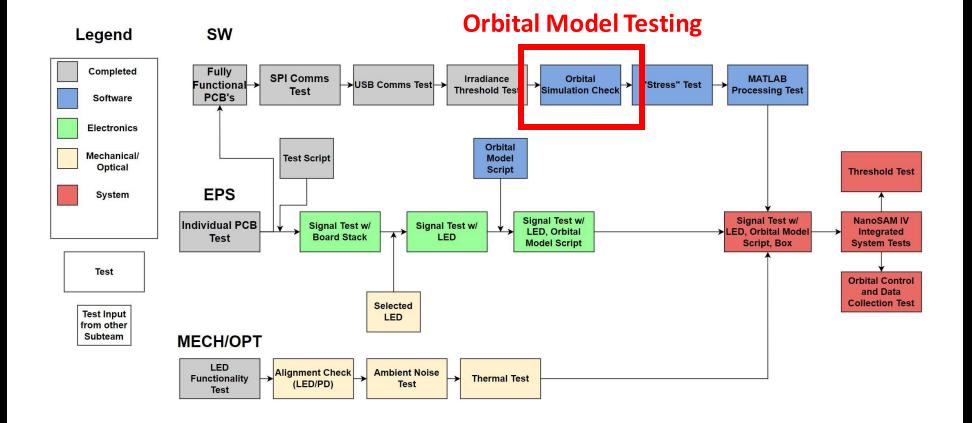






Test #2 – Orbital Model





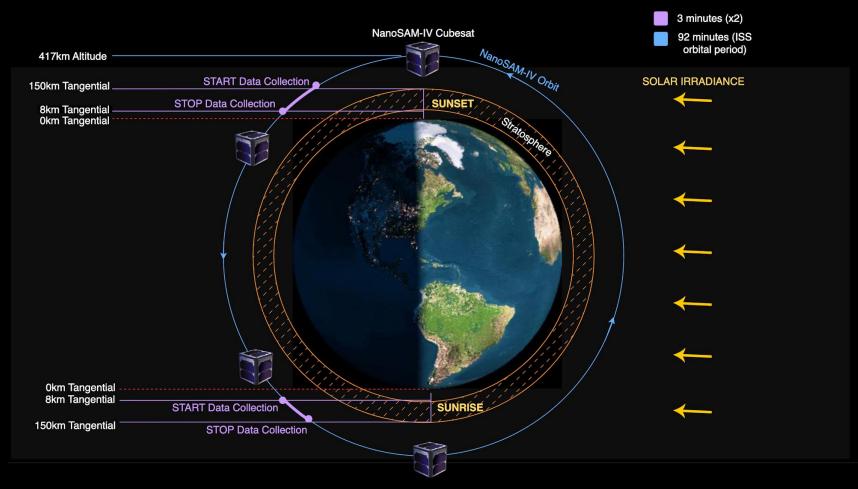
Project Overview

Schedule

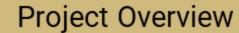




Orbital Simulation

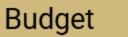


NanoSAM-IV Orbital Model ConOps



Schedule

Test Readiness



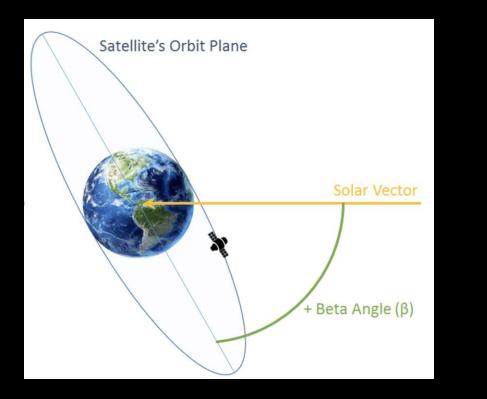
30

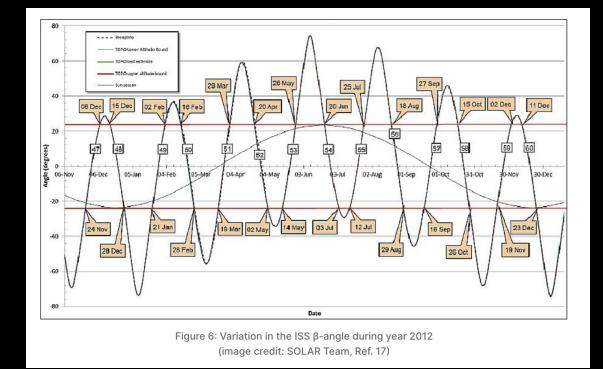
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Orbital Simulation







Beta Angle Variation Over the Course of 1 Year

Solar Beta Angle Visualization

Project Overview

Schedule

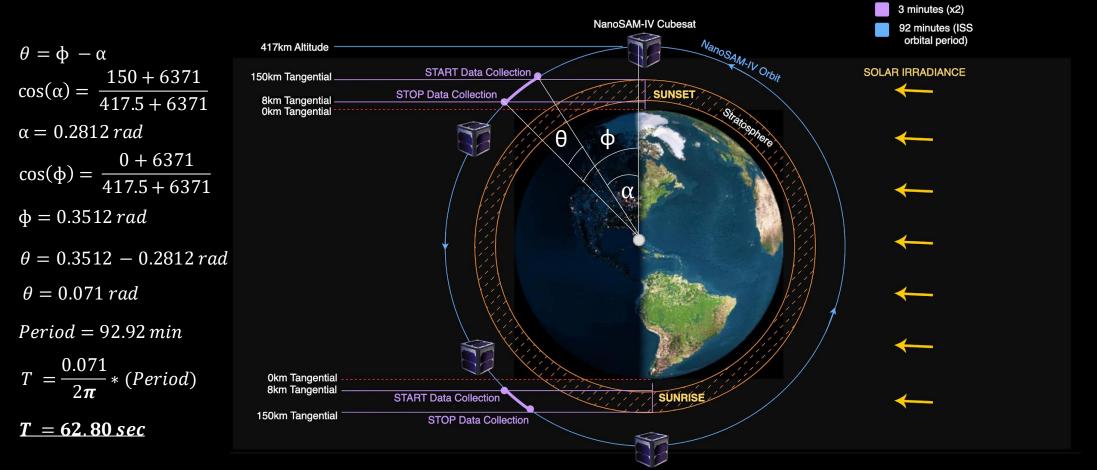
Test Readiness

Budget



Math for $Beta = 0^{\circ}$





Project Overview

Schedule





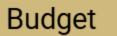


Mission Objectives

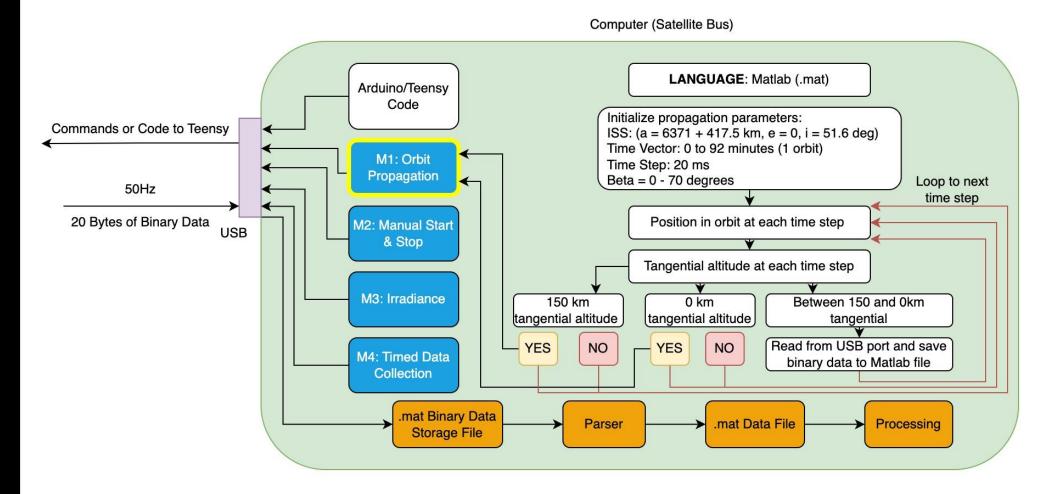
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MO-03	Autonomous control of data collection process through timing that matches predicted orbital data collection windows
MO-04	Instrument must be appraised of when intensity of light source falls below a threshold

Project Overview

Schedule



Test Description







Testing Procedure

Tangent altitude at each time step
 Plot tangent altitude vs. time for 1 orbital period
 Identify times to issue commands to Teensy (USB)
 Identify times to read data from Teensy (USB)

Beta = 0° Beta = 30° Beta = 60° Beta = 70°

Project Overview

Schedule





Testing Criteria



	Indication of Sunrise and Sunset Event	Time of Sunset or Sunrise Data Collection	Tangent Altitude at Each Time Step	Maximum Tangent Altitude	Minimum Tangent Altitude	Start or Stop Data Collection Commands Issued
Beta = 0°	Yes	62.80 sec	Yes	417.5 km	Negative Value (km)	4
Beta = 30°	Yes	62.80 < X < 180 sec	Yes	417.5 km	Negative Value (km)	4
Beta = 60°	Yes	62.80 < X < 180 sec Larger than Beta = 30°	Yes	417.5 km	Negative Value (km)	4
Beta = 70°	No	N/A	Yes	417.5 km	Positive Value (km)	0

Project Overview

Schedule

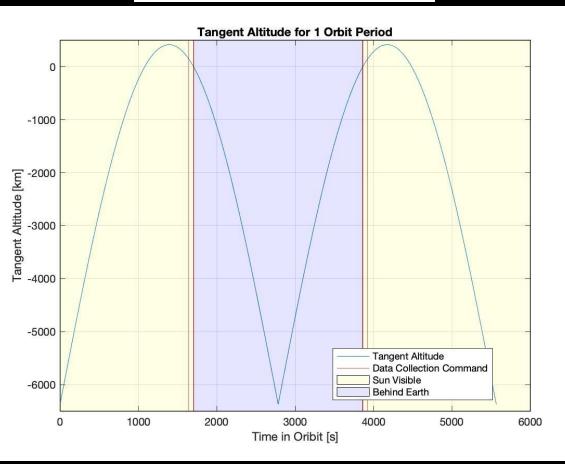


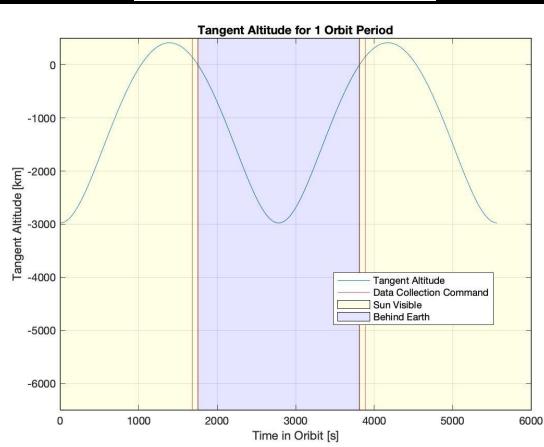


Orbital Simulation Test Results



Duration of Occultation Event: 62.80 s





Duration of Occultation Event: 73.84 s

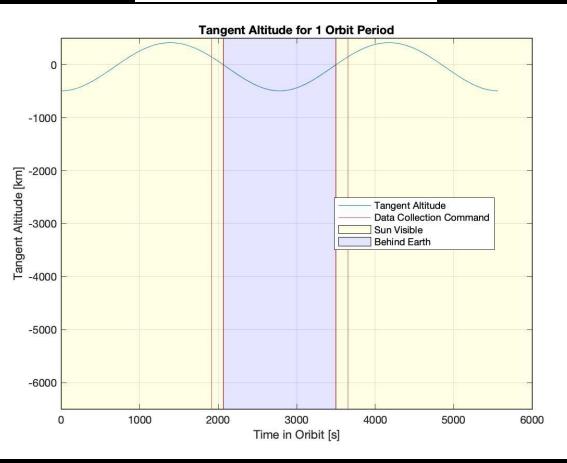
Beta = 0°

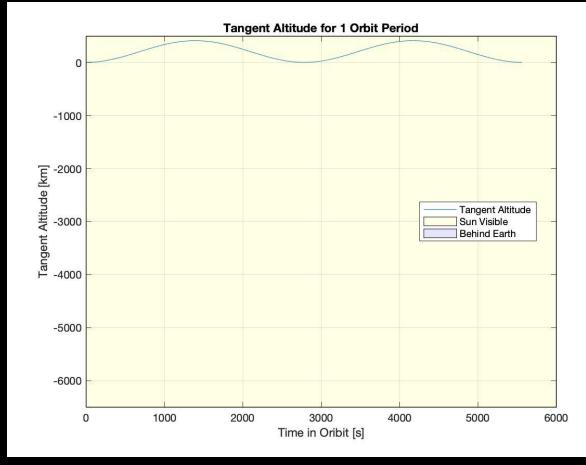


Orbital Simulation Test Results



Duration of Occultation Event: 153.10 s





Beta = 70°





Orbit Simulation Risk Reduction

Test Pass	Verification of code for future debugging	Proceed to Phase 3 – able to begin integration with full system
Test Failure	Potential future errors in the system	Delay of Phase 2

Project Overview

Schedule

Test Readiness

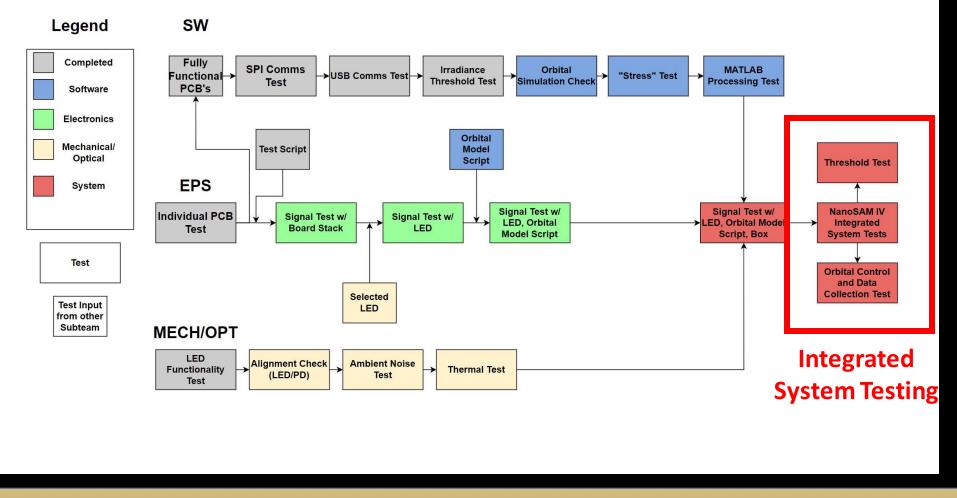
Budget

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Test #3 – Integrated System



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System Testing - Objectives

MO-01	Generate light signal that will simulate expected on-orbit, un-attenuated signal in order to test embedded system (photodiode, signal cond., ADC, microcontroller)
MO-02	Integrate embedded system design from NanoSAM II (Analog and Digital PCB's) to collect data with an SNR of 200 or greater
MO-03	Autonomous control of data collection process through timing that matches predicted orbital data collection windows
MO-04	Instrument must be appraised of when intensity of light source falls below a threshold

Project Overview

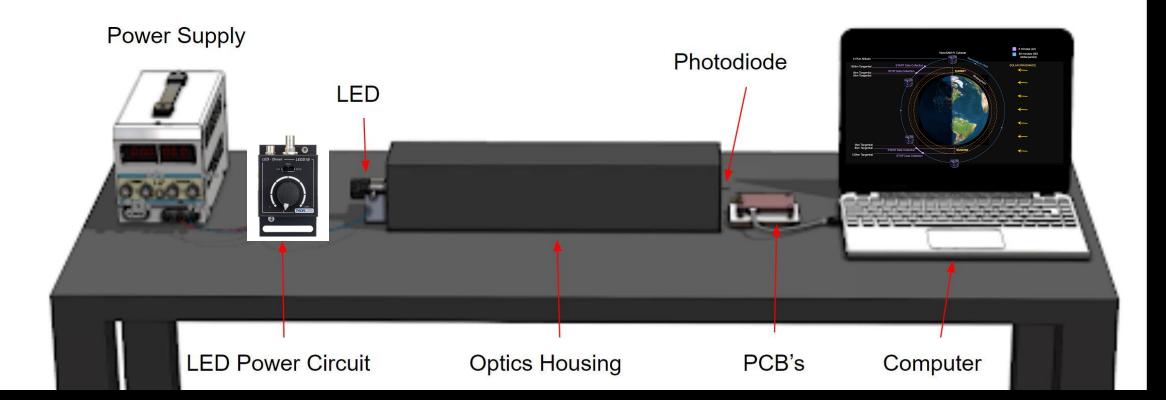
Schedule





System Testing Setup





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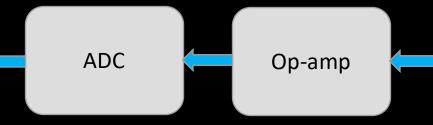
42



System Testing – OCDC Test



3. Photodiode signal conditioning and digitization



2. Teensy receives data from ADC, packages it and sends it to computer



 Software propagates orbital data, and issues commands to and receives data from Teensy 4. Photodiode produces 0.475 uA current as light strikes it



5. LED driver provides power to LED, LED emits light at 680 nm with 210 mW power output





OCDC Test – Key Performance Metrics



- **Data Collection Windows:** Data collection should start when propagated orbital data gives tangent altitude of 150 km (sunset) or 0 km (sunrise). Stop when tangent altitude is 0 km (sunset) or 150 km(sunrise)
- **Op-amp output**: Should remain around 1 V +/- noise levels measured in SNR testing
- **Orbit Data**: Should stop propagating after 1 full orbit and match data from Orbit Model test





System Testing – Threshold Test



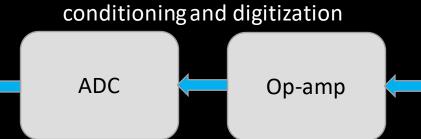


Noise causes Photodiode

to produce current

4.





3. Photodiode signal

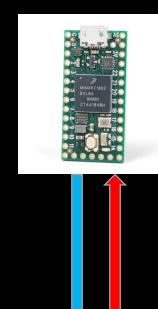
 Teensy receives data from ADC, adds bin value to 200 SNR bin value and stores it as threshold bin value



 Software issues command to Teensy

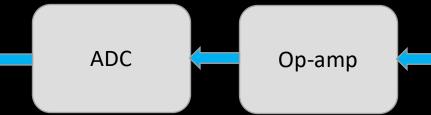






System Testing – Threshold Test

3. Photodiode signal conditioning and digitization



- 2. Teensy receives data from ADC, compares bin value to threshold bin value and sets flag accordingly, sends data to computer
- TO AAM TO AND TO
- Software issues commands to and receives data from Teensy

4. Photodiode current will increase or decrease with LED output power



5. LED output power will be adjusted using current knob

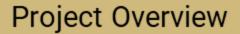




Threshold Test – Key Performance Metrics



- **Threshold Flag:** Should be set to high when the first data point below the threshold is measured. Should be set to low when the first data point above the threshold is measured
- **Op-amp output**: Should remain around 1 V +/- noise levels measured in SNR testing



Schedule







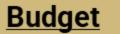


Budget

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Budget Breakdown

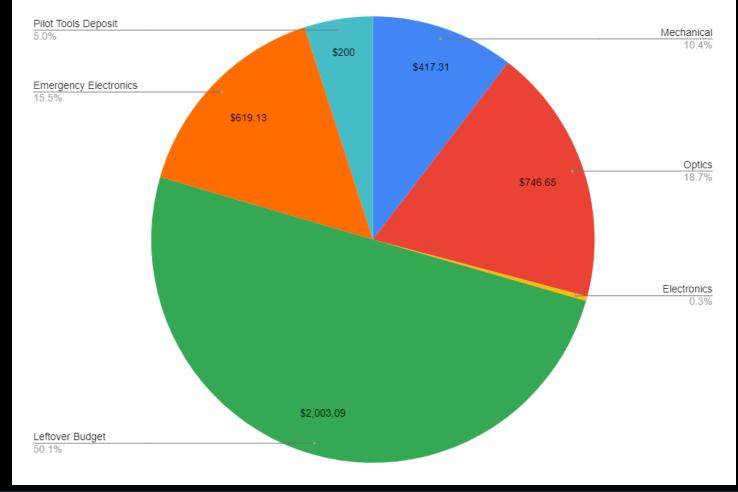


Current Budget Left as of 2/27/2023: **\$2622.22**

- 15.5% of the budget is allocated for the purchase of emergency electronics – as this is the area with the highest risk of failure
- 18.7% of the budget are the LED and LED Current Driver Critical hardware

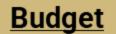
Current Budget Left with use of emergency electronics: **\$2003.09**

Budget Breakdown



Project Overview

Schedule





Electronics Uncertainty/Extra Cost Margin

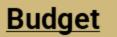


- In the case of NanoSAM-II Board Failure/Short Circuit
 - A cost plan has been made with 30% margins to account for emergency fabrication of analog and digital boards if applicable
- Based on recent testing of analog and digital boards, manufacturing new boards is very unlikely

NanoSAM-II Board Revision Summary									
Board		Analog		Digital					
PCB Manufacturing	\$	30.00	\$	30.00					
Parts for Inhouse Population	\$	41.35	\$	57.41					
Quantity		3		3					
Subtotal	\$	214.04	\$	262.22					
Fiscal Margin		30%		30%					
Total	\$	278.25	\$	340.88					
Overall To		\$	619.13						

Project Overview

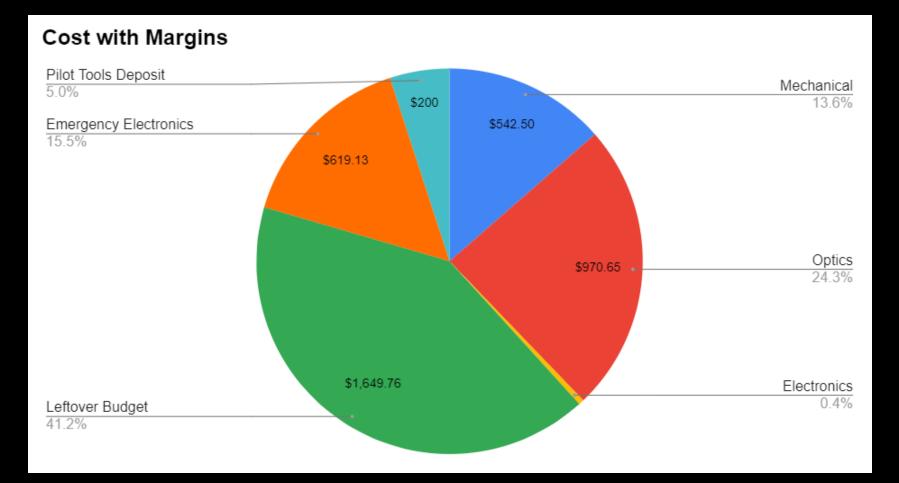
Schedule





Budget Breakdown with Margins





30% Margins for all Subsystems

- Mechanical (\$417.31)
 - \$125.19
- Optics (\$746.65)
 - \$224.00
- Electronics (\$13.82)
 - \$4.15

After additional margins and emergency considerations; leftover budget of <u>\$1649.76</u>

Project Overview

Schedule



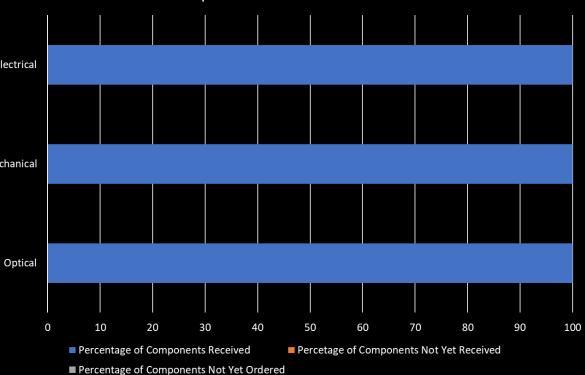


Received Components



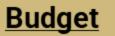
Price	Subteam	Component
\$322.36	Optics	M680L4 LED, FD11A Photodiode and Connectors for Light Source Circuit
\$424.29	Optics	T-Cube LED Driver
\$13.82	Electronics	Resistor and Capacitor for board revisions
\$9.56	Mechanical	Putty to hold thermistor in place
\$68.87	Mechanical	Paint, epoxy and PLA for Optics bench and prototype bench manufacturing
\$178.22	Mechanical	Aluminum for manufacturing of the Optical Bench
\$97.87	Mechanical	Safety equipment and painting supplies for the optics bench
\$62.79	Mechanical	More PLA for the prototype bench

Component Procurement Status



Project Overview

Schedule









Questions?







Backup Content



SNR Testing – Signal Test 1



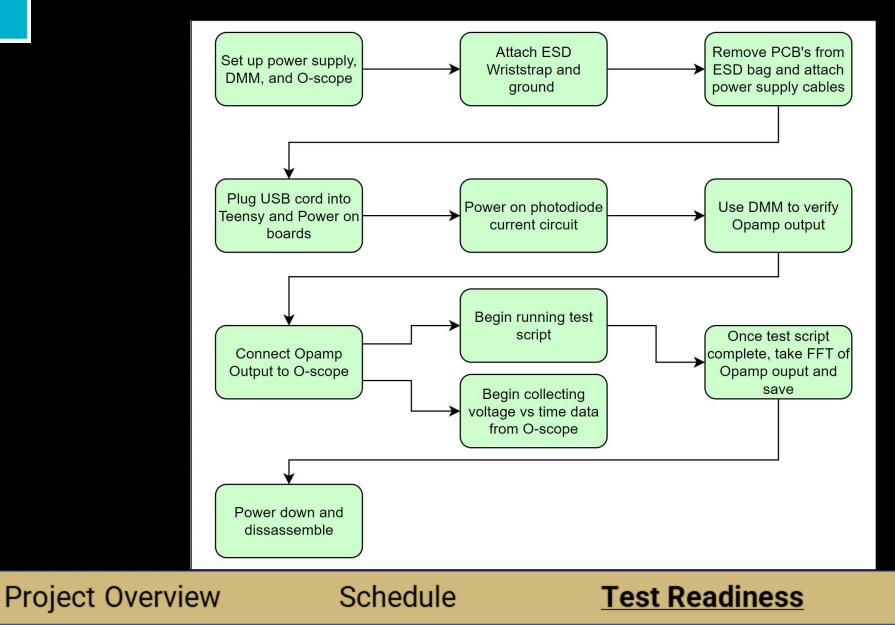
- In this sub-test, the mock photodiode circuit will facilitate data collection and noise measurements of Op-amp and ADC.
- Testing involves:
 - 1. Supplying power to boards and mock photodiode circuit
 - 2. Measuring the output of the op-amp with the oscilloscope
 - 3. ADC output will be simultaneously recorded and compared to op-amp output to determine noise
 - 4. Taking FFT of op-amp output (process using MATLAB) to determine its noise
- Test equipment / Facilities
 - Oscilloscope
 - ➤ Computer
 - Power supply
 - ➢ Farraday Cage
 - Electronics Lab Space

Schedule





Signal Test I Flow





Budget



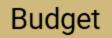
SNR Testing – Signal Test 2



- In this sub-test, data will be collected with the actual photodiode and its noise will be estimated.
- Testing involves:
 - 1. Setup prototype LED-Photodiode housing
 - 2. Supplying power to boards and LED
 - 3. Collect data from Teensy over USB
 - 4. Measure photodiode output with DMM and manually record current values in regular time intervals
- Test Equipment / Facilities:
 - Oscilloscope
 - Digital Multimeter
 - Computer
 - LED/LED Driver
 - Farraday Cage
 - Prototype Optical Housing Assembly
 - Power Supply
 - Electronics Lab Space

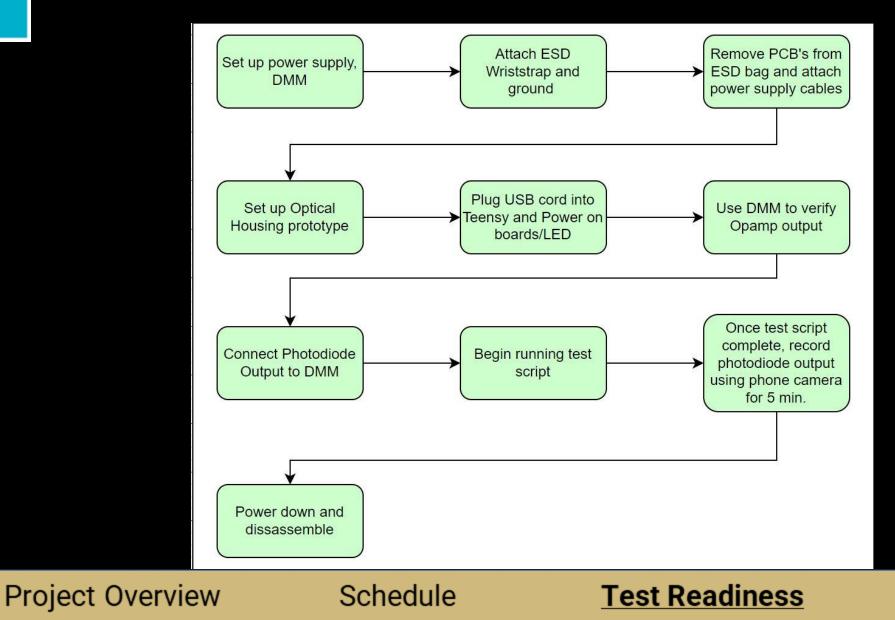
Project Overview

Schedule





Signal Test II Flow





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Budget



SNR Testing – Key Performance Metrics

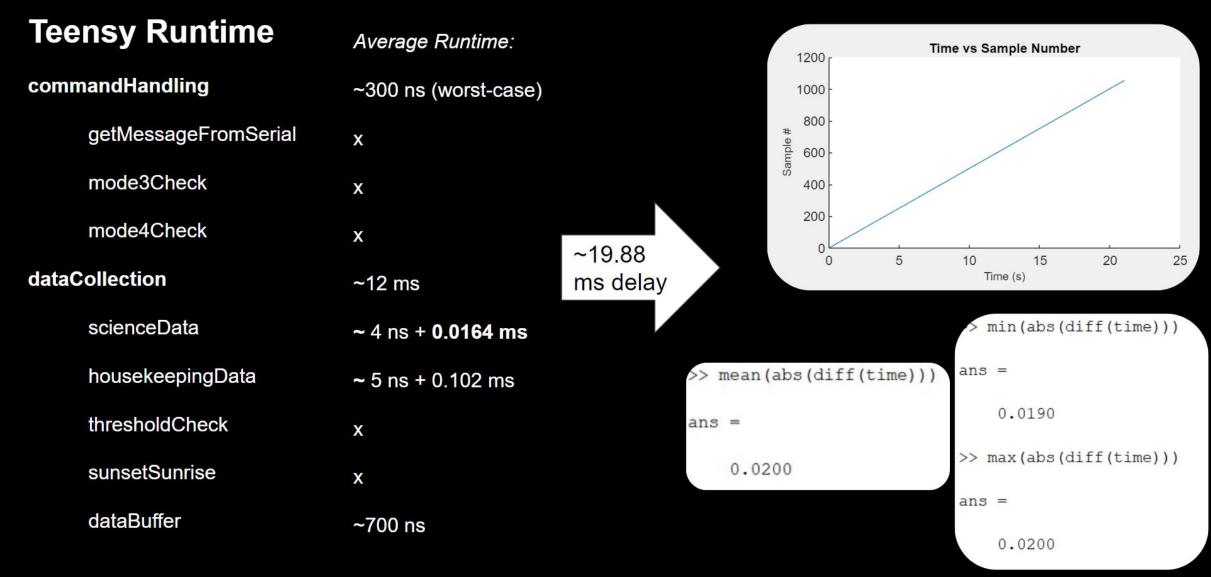


- **ADC Noise**: Can not exceed BLANK mV (expected = BLANK mV)*
- **Op-amp Noise**: Can not exceed BLANK mV (expected = BLANK mV)*
- **Photodiode Noise**: Can not exceed BLANK uA (expected = BLANK uA)*
- **SNR***: Should occur at nominal photocurrent, 0.475 uA (expected SNR = 1224.3)
- **200 SNR**: should occur at photocurrent of 77.6 nA
- **Upper Bound SNR**: should occur at photocurrent of 594 nA (expected = 1530.4)
- * = at Nominal Photocurrent (0.475 uA)

Schedule



Timing Software Testing



Data Processing Software Testing

Data Collection

l c	lataRaw 🛛 🗶																			
10	053x20 uint8																			
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
1	65	9	57	0	0	204	204	97	115	6	0	6	0	6	5	0 (5 C) (5 0	90
2	65	28	57	0	0	204	204	97	115	6	0	5	0	6	5	0 (5 C) (5 0	90
3	65	48	57	0	0	204	204	97	115	6	0	5	0	6	5	0 (5 0) (5 0	90
4	65	68	57	0	0	204	204	97	115	6	0	4	0	6	5	0 5	5 0) 7	7 0	90
5	65	88	57	0	0	204	204	97	115	6	0	5	0	6	5	0 (5 C) (5 0	90
6	65	108	57	0	0	204	204	97	115	6	0	6	0	6	5	0 (5 C) (5 0	90
7	65	128	57	0	0	204	204	97	115	6	0	6	0	6	5	0 (5 C) (5 0	90
8	65	148	57	0	0	204	204	97	115	6	0	5	0	6	5	0 6	5 C) (5 0	90
9	65	168	57	0	0	204	204	97	115	6	0	5	0	6	5	0 (5 C) (5 0	90
10	65	188	57	0	0	204	204	97	115	6	0	5	0	6	5	0 (5 0) (5 0	90

Data Parsing

Data Cleaning/Converting

l∫ d	dataParse 🛛 🗙											1	dataConvert 🚿
10	053x11 double	e											1053x11 double
	1	2	3	4	5	6	7	8	9	10	11		1
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2	65	14620	52428	97	115	6	5	6	6	6	90		2 65
3	65	14640	52428	97	115	6	5	6	6	6	90		3 65
4	65	14660	52428	97	115	6	4	6	5	7	90		4 65
5	65	14680	52428	97	115	6	5	6	6	6	90		5 65
6	65	14700	52428	97	115	6	6	6	6	6	90		6 65
7	65	14720	52428	97	115	6	6	6	6	6	90		7 65
8	65	14740	52428	97	115	6	5	6	6	6	90		8 65
9	65	14760	52428	97	115	6	5	6	6	6	90		9 65
10	65	14780	52428	97	115	6	5	6	6	6	90	1	0 65
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1	lataConvert	×										
10	053x11 doubl	e										
	1	2	3	4	5	6	7	8	9	10	11	
1	65	0	1.0000	97	115	6	6	6	6	6	90	
2	65	0.0190	1.0000	97	115	6	5	6	6	6	90	
3	65	0.0390	1.0000	97	115	6	5	6	6	6	90	
4	65	0.0590	1.0000	97	115	6	4	6	5	7	90	
5	65	0.0790	1.0000	97	115	6	5	6	6	6	90	
6	65	0.0990	1.0000	97	115	6	6	6	6	6	90	
7	65	0.1190	1.0000	97	115	6	6	6	6	6	90	
8	65	0.1390	1.0000	97	115	6	5	6	6	6	90	
9	65	0.1590	1.0000	97	115	6	5	6	6	6	90	
10	65	0.1790	1.0000	97	115	6	5	6	6	6	90	

Threshold Testing & Timed Collection Testing

Threshold Check

```
void mode3Check(){
    if (mode3 & (thresholdCount >= 100)){
        collect = 0;
        mode3 = 0;
    }
```

Timed Check

```
void mode4Check(){
  if (!mode4)
    return;
  mode4count++;
  if (mode4count/50 >= mode4time){
    collect = 0;
    mode4count = 0;
    mode4time = 0;
}
```

Commands & Global Variables

Command Handling /	Global Variables
Condition	Changes
Orbital Model Mode	collect = True
Start: @M1	mode1 = True
Orbital Model Mode Stop: @S1	collect = False mode1 = False swap(sunsetCondition, sunriseCondition)
Manual Mode	collect = True
Start: @M2	mode2 = True
Manual Mode	collect = False
Stop: @S2	mode2 = False
Irradiance Threshold Mode	collect = True
Start: @M3	mode3 = True
Irradiance Threshold Mode Stop Condition: (threshholdCount >100)	collect = False mode3 = False
Timed Data Collect Mode Start: @M4XXX XXX is time in seconds	collect = True mode4 = True m4Count = 0 m4Max = XXX*50
Timed Data Collect Mode Stop Condition: (m4Count >m4Max)	collect = False mode4 = False



System Testing - Objectives



•The integrated system test is divided into two sub-tests: Orbital Control and Data Collection (OCDC) Test and Threshold Test

•Goal: to satisfy remaining mission objectives and is the main deliverable of the NanoSAM IV project

•The testing will satisfy the following requirements:

•MO-03: Autonomous Control of data collection process through timing that matches predicted orbital data collection windows

•MO-04: Instrument must be appraised of when intensity of light source falls below an experimentally determined noise value





System Testing – Pre-requisite tests



- **Retrofitted and tested PCB's**: Voltage measurements were taken at all test points of the boards and actual values matched our expected measurement values
- **Completed data collection script**: Integrated and tested orbital model and data collection loop
- **Completed "Mode 3" Test Script**: software to determine the threshold value, collect and output threshold flag and irradiance data
- **Final optical housing assembly**: Finalized LED/Photodiode housing
- **LED equipment**: Functionality of the LED was verified and validated

Schedule





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System Testing – OCDC Test



• In this sub-test, data collection will be autonomously controlled using orbital simulation. Data will be packetized according to CCSDS protocol and transferred to computer over USB for processing





System Testing – Threshold Test



- In this sub-test, the LED output power will be gradually lowered past until the threshold intensity is measured by the embedded system. The power will then be raised back up and threshold flag output will be recorded
- Testing involves:
 - 1. Setup LED-Photodiode housing
 - 2. Supplying power to boards and LED
 - 3. Begin lowering LED supply current until Threshold flag is set to high, then raise back up until flag is low again
 - 4. Save Threshold flag output along with timestamps and photodiode output
- Test Equipment / Facilities:
 - LED/LED Driver
 - Optical Housing Assembly
 - Digital Multimeter
 - Farraday Cage
 - Computer
 - Power Supply
- Requirements Test will satisfy: MO-04





Changes to Schedule (Phase I)



		Task Scheduled																							
		Built In Margin																							
	Legend	Old Critical Path																							
		New Critical Path											2	023											
								January																	
	Т	ask	Sub-teams	End Date	Buffer/Margin [+ days]	Total Days (including margin)	1/15 1/16	1/17 1/18 1/19	1/20 1/21	1/22 1/23	1/24 1/25	1/26 1/27	1/28	1/29	1/30 1/3	1 2/1	2/2	2/3	2/4	2/5	2/6	2/7	2/8 2	2/9 2/10) 2/11
Status			Winter Break																						
Status	Manufacturing/F	reliminary Testing														_									
complete		LED Functionality Check	Mechanical/Opti cs	1/31/2023	3	18																			
complete		Manufacture Box/Optics Apparatus	Mechanical/Opti cs	1/31/2023	3	18					_														
complete		SPI Comms Test	Software	1/31/2023	3	18									>										
complete		USB Comms Test	Software	1/31/2023	3	18																			
complete		Signal Test I	Electronics	1/31/2023	3	18																			
complete		AIAA Abstract	All	2/2/2023	9	18												DRAFT							DUE!
complete		Internal Design Review (IDR)	All	2/7/2023	3	20																		DUE	

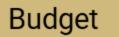
Although complete now, some tasks were behind schedule at first Signal Test I & Manufacturing Optics Test Apparatus Delays primarily due to parts/materials logistics (i.e. ordering & delivery)

Extra week buffer added to Phase I tests to accommodate actual IDR deadlines

NEW: Decision made to prototype optics test apparatus via 3D Printing before manufacturing with aluminum

Project Overview

Schedule





Changes to Schedule (Phase II)





												2023					
						Janary		February								March	
	Task Sub-teams End Date Buffer/Margin (+ days) trotal Days			Total Days (including margin)	1/22 1/23 1/24 1/25 1/26 1/27 1/28	1/29 1/30 1/31	2/1 2/2 2/3 2/4	2/5 2/6 2/7	2/8 2/9 2/10 2/11	2/12 2/13 2/14 2/15 2/16 2/17 2/18	2/19 2/20 2/21 2/22 2/23 2/24 2/25	2/26 2/27 2/2	3 3/1 3/	2 3/3 3/4	3/5 3/6 3/7 3/8 3/9 3/10 3/11	3/12 3/13 3/14 3/15 3/16 3/17 3/18	
Status	Main Testing																
complete	Threshold Irradiance Check		2/11/2023	3	14												
complete	Alignment Check	Mechanical/Opti cs	2/11/2023	3	14												
complete	Orbital Simulation Check	Software	2/11/2023	3	14									<			
in progress	Signal Test II	Electronics	2/11/2023	3	14									<			
in progress	"Stress Test"	Software	2/22/2023	3	14									Ð			
in progress	Finished Assembly	Mechanical/Opti cs	2/22/2023	3	14									ar			
delayed	Signal Test III	Electronics	2/22/2023	3	14									Ū.			
delayed	"Dark" Test	Mechanical/Opti cs	2/25/2023	2	9									Ч			
delayed	Thermal Test	Mechanical/Opti cs	2/25/2023	2	9									er			
in progress	Test Readiness Review (TRR)	All	2/21/2023	7	19								DUEI	æ			
in progress	AIAA Paper	All	2/26/2023	17	41											1st DRA FT DUE	FINA L DRA FT DUE

Signal Test I took longer than originally projected Signal Test II & III delayed as a result

Since prototyping was 3D printed, final aluminum assembly & alignment postponed Avoided possible wasted time/material & re-order expensive aluminum if calculations were off Many tests may still move forward with 3D printed version of apparatus (such as dark/thermal tests & SNR)

Software Stress Test delayed

May be finished in parallel with "Finalize Code" in Phase III Testing

Project Overview

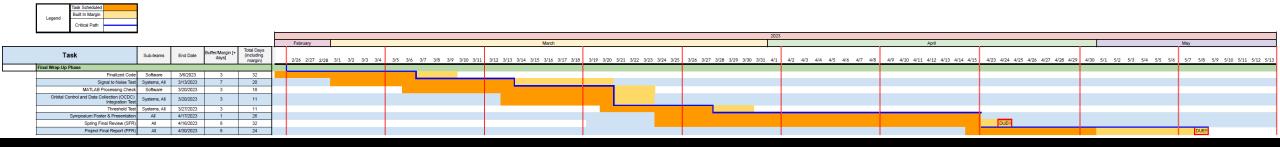
Schedule





Phase III: How We'll Stay On Schedule





Finalizing Code: can be done in parallel with software "stress test" (which is in progress/nearly finished)

Signal to Noise Test: High Risk!

Direct Dependence on Signal Tests II & III, Finished Assembly, Dark & Thermal Tests (assign more help to these tasks if necessary)

Orbit Control/Data Collection: depends on Orbital Simulation Software (complete)

<u>Threshold Test:</u> Depends on SNR Test & MATLAB Processing (assign more help if necessary)

Project Overview

Schedule





Previous, Current & Future Tests

Or

Test to be Performed



Completed or Ongoing Tests Tests Dependent On This Test Test to be Performed LED Functionality Check Alignment, Signal Test II Manufacture Box/Optics Apparatus Alignment SPI Comms Test SNR & Final Code **USB** Comms Test Stress, Threshold Irradiance Signal Test I Signal Test II & III, Alignment Threshold Irradiance Check SNR & Final Threshold Test Alignment Check Finished Assembly Orbital Simulation Software Check Stress Test Signal Test II Signal Test III Signal Test III Stress Test Finished Assembly Dark & Thermal Tests Signal Test III Signal to Noise (SNR) Test Dark Test SNR & Final Threshold Test

SNR & Final Threshold Test

Status Complete Complete Complete Complete Complete Complete Complete Complete In Progress In Progress Complete Delayed Delayed Delayed

Upcoming Testing rformed Tests This Depends On Projected Completion Date Finalized Code USB & SPI Comms, Orbital Model March 6th

Signal to Noise Test	Signal Tests I, II, & III, Threshold Irradiance, Thermal Test, Dark Test	March 13th
MATLAB Processing Check	All Software Tests & Tasks	March 20th
orbital Control and Data Collection (OCDC) Integration Test	USB & SPI Comms, Orbital Model; All Software Tests & Tasks	March 20th
Threshold Test	SNR & Matlab Processing	March 27th

Project Overview

Thermal Test

Schedule







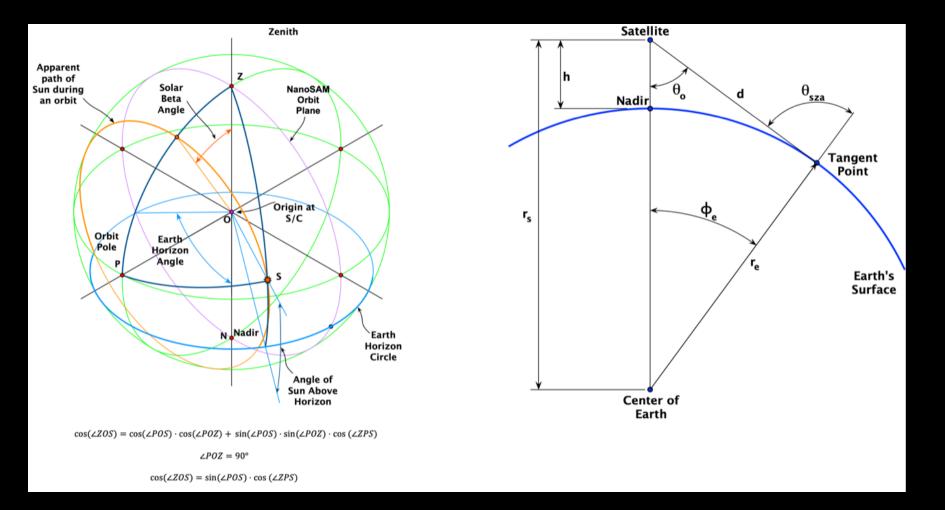
Related Requirements - Backup

MO-03	Autonomous control of data collection process through timing that matches predicted orbital data collection windows
R1	Desktop computer or laptop (mock spacecraft bus) will house this control software
R2	Computer will send commands to the payload instrument
R3	Computer will receive data from the payload instrument
R4	Commands will be synched up with the orbital simulation
R5	Simulate spacecraft position on a circular orbit for at least 1 orbital period
R6	Start and stop of data collection triggered by simulated orbital position
R7	Data sampling rate is 50 Hz
R8	Two collection windows per orbit (sunset and sunrise)
R9	Start and stop data collection at 0 km and 150 km tangential to the surface of the earth depending on sunrise or sunset conditions
R10	Computer will store all data (received payload data and simulation data)





Orbital Simulation Test - Backup



Body-Centered Spacecraft Frame

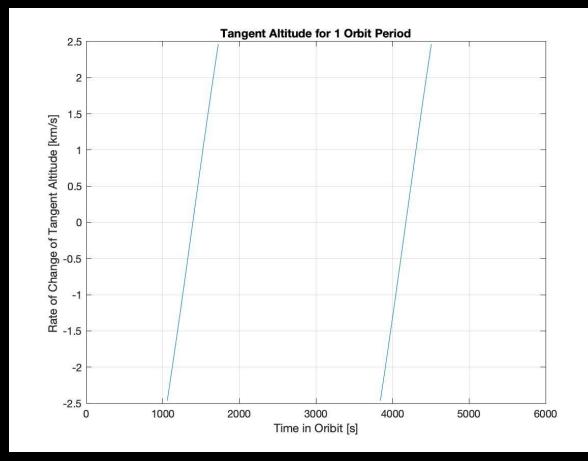




Orbit Simulation Test - Backup

- Largest rate of change when tangent altitude is near zero
- ~2.5km/s max rate (Beta = 0°)
- Corresponds with worst resolution being 0.053 km with 20 ms time steps

tangAlts 🗶		
Η 278319x1 double		
	1	2
53960	NaN	
53961	NaN	
53962	NaN	
53963	NaN	
53964	NaN	
53965	0.0207	
53966	0.0736	
53967	0.1265	
53968	0.1795	
53969	0.2324	
53970	0.2853	



Project Overview

Schedule

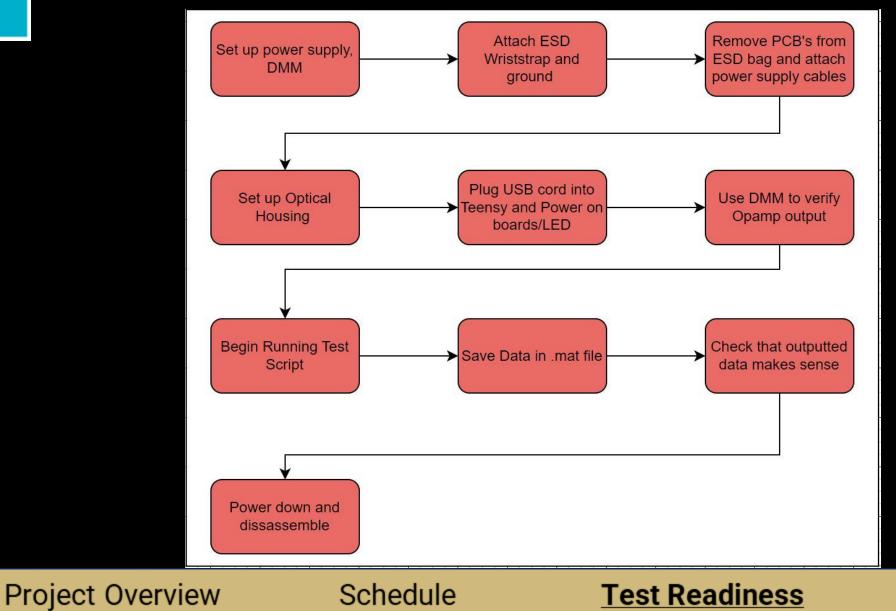
Test Readiness

Budget

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OCDC Test Flow

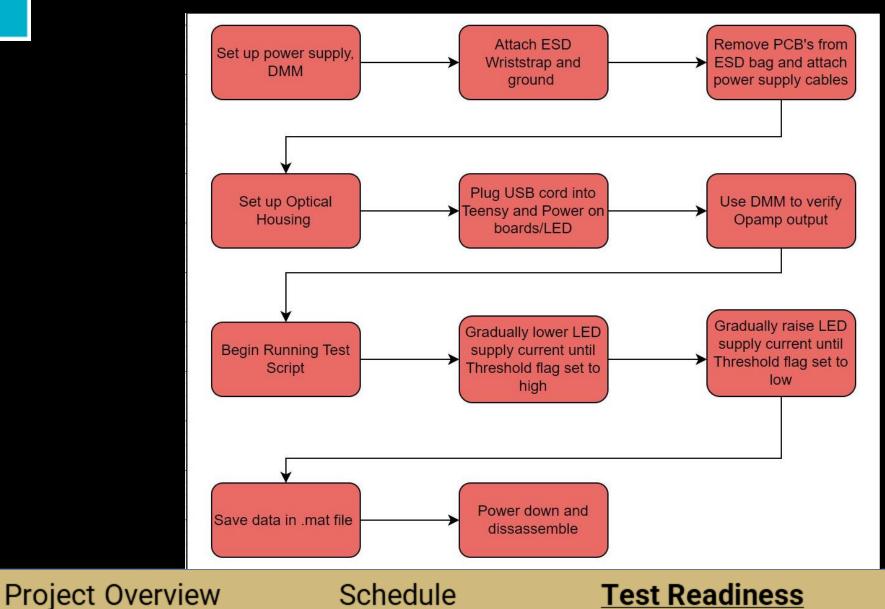




Budget



Threshold Test Flow

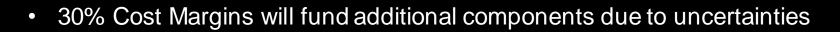




Budget



Budget Uncertainties



- Electronics
 - Extra electronics components if not already possessed
- Mechanical
 - Additional materials, paint, manufacturing equipment
- Optical
 - Additional photodiode/thermistors

Project Overview

Schedule

