### NanoSAM II

Nano-Stratospheric Aerosol Measurement

**Preliminary Design Review** 

September 13, 2020



Ball – &

University of Colorado Boulder
 Department of Aerospace Engineering Sciences

### Agenda



- Evidence of Baseline Feasibility
  - Enclosure
  - Electronics
  - Software
- Status Summary & Remaining Studies

# Project Description

Abby Hause



Baseline Feasibility



### Previous Work and Heritage

### **SAM II** (1975-1996)



First attempt at stratospheric aerosol measurement on orbit

## **SAGE I, II, III** (1979-2020)



Measures multiple wavelengths Large & expensive Data quantity limited by orbital period NanoSAM I (2019-2020)



Developed an optical system to meet or exceed the capabilities of SAM II in a CubeSat footprint

**Baseline Feasibility** 

 $\rightarrow$ 











## Objectives









### **Functional Requirements**



### **Baseline** Design



## Electronics

Jashan Chopra

**Project Description** 







### Relevant Requirements

### 1.0: Data Capture

- Communicates via photodiode with optical bench
- Samples, processes, and stores data at a rate greater than 50 Hz
- Data is collected in a 10 bit resolution
- Enough storage space for downlink periods
- Housekeeping data is stored (temperature, power usage)
- The system uses less than 8W

### 2.0: Communications

- Comms with laptop for ground testing, standard bus system for flight
- Signal to noise ratio greater than 3500

### 4.0: Payload Dimensions

- Payload size must be less than 0.5U
- Payload mass less than 0.615 kg

#### Photodiode attached via twisted pair shielded wire

- 16 bit ADC, 833 Hz sampling
- External Flash provides 256 Mb of space
- Thermistor temperature monitoring
- Analog inputs on microcontroller for other housekeeping
- USB I/O for testing and bus communications

#### **Project Description**

Baseline Feasibility (Electronics)



### **Key Characteristics**



#### Project Description

Baseline Feasibility (Electronics)

### Power Budget

**Power Requirement** 8 W

**Estimated Power Usage** (worst case scenario) ~7.17 W w/ heater ~1.66 W w/o heater

**Power Usage Ratio** 89.6%

Power estimates for digital I.C's are based off maximum DC characteristics, and are not representative of the typical power draw, merely the maximum possible at any given point

**Project Description** 

#### **Power Consumption**



Status Summarv

Baseline Feasibility (Electronics)

Percentages are of the 1.66 W

board component power usage

### Signal to Noise Ratio (SNR)



#### Noise Sources [ref. 5]

#### Photodiode

Dark Current Johnson Noise Shot Noise 1/f noise

#### Circuitry / Transmission

Loss in op-amp 5V Regulator Uncertainty Quantization noise (ADC) ADC Offset Noise

**Status Summary** 

**Project Description** 

**Baseline Feasibility (Electronics)** 

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## Enclosure

David Perkins & Danny Barth

**Project Description** 







### Relevant Requirements

#### 4.0: Payload Dimensions

- Payload size must be less than 0.5U
- Payload mass less than 0.615 kg

### 5.0: Flight Testing

- Payload maintains functionality in space
- Payload can survive vibrational, thermal, and vacuum testing
  - No resonant frequencies below 100Hz [1]
  - Survives a temperature range of -20°C to 50°C [1]

- Current dimensions: 9.6cm x
  9.8cm x 4.6cm
- Current mass of .276kg w/o filter or light-blocking walls
- Estimated Maximum Total Mass: .450kg
- Lowest resonant frequency: ~385Hz
- $P_{heater,max} = 5.46W$
- \$425 (Projected, 2.5x FOS)

#### **Project Description**

Baseline Feasibility (Enclosure)

### Enclosure – Structural Model Baseline





#### **Project Description**

Baseline Feasibility (Enclosure)

### Enclosure – Structural Model Baseline



### Enclosure – Structural Model

#### **Resonant Frequency Preliminary Study**

- Meshed in Solidworks
  - Removed any threads or interfacing screws
  - Two contact methods analyzed
    - Screws  $\rightarrow$  Pin Joints
    - Autointerface
- Boundary Conditions
  - One fixed corner (green on right)
  - Expect these to evolve, but current results demonstrate feasibility



#### Simplified Finite Element Model

**Status Summary** 

Baseline Feasibility (Enclosure)

### Structural Model - Vibrational Simulation



Baseline Feasibility (Enclosure)

**Project Description** 

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### Thermal Model Diagram



Project Description

Baseline Feasibility (Enclosure)

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### Thermal Model Assumptions/Equations

#### Key Assumptions:

- Parallel rays from the sun
- Albedo/longwave radiation travels parallel along the radial line from the Earth to the satellite
- Bodies other than the Earth/Sun don't produce significant incident radiation
- Lumped system
- Kirchoff's law ( $\epsilon_{\lambda}(T) = \alpha_{\lambda}(T)$ )
- Satellite surfaces are gray, diffuse, surfaces
- T<sub>surr</sub> = 0 K

### Key Equations:

$$T_{sys,eq} = \left[\frac{\dot{Q}_{in,sun} + \dot{Q}_{in,A} + \dot{Q}_{in,IR} + P_i}{2\sigma\epsilon(2A_s + A_p)}\right]^{(1/4)}$$

$$\dot{Q}_{tot} = \dot{Q}_{in,A} + \dot{Q}_{in,IR} + \dot{Q}_{in,sun} + P_i - \dot{Q}_{out}$$

$$T_i = \frac{\dot{Q}_{tot}\Delta t}{mc_p} + T_{i-1}$$

Status Summarv

Baseline Feasibility (Enclosure)

### Passive Thermal Control Results

#### **Equilibrium Results:**



#### Transient Results:



**Project Description** 

Baseline Feasibility (Enclosure)

### Active Thermal Control Results

#### **Equilibrium Results: Transient Results:** Equilibrium Temperature with Heater On Transient Temperature Change with Heater On $(\beta = 23.5^{\circ}, P_{heater} = 5.46 \text{W})$ $(\beta = 23.5^{\circ}, P_{heater} = 5.46 \text{W})$ 20 15 -Cold Case (SG121FD White Paint) Cold Case (SG121FD White Paint) Hot Case (SG121FD White Paint) Hot Case (SG121FD White Paint) 10 10 System Temperature (°C) bb 01 0 System Temperature (°C) 5 -5 -10 -30 -20 -40 -25 0.2 0.4 0.6 0.8 0 2 8 10 0 6 t/T t/T

**NOTE :** P<sub>heater</sub> does not include power dissipated by the electronics board passively

#### Project Description

**Baseline Feasibility (Enclosure)** 

## Software

Jackson Kistler

**Project Description** 





### Relevant Requirements

#### 1.0: Data Capture

- Data collection is timed to capture both sunrise and sunset through the stratosphere
- Samples, processes, and stores data at a rate greater than 50 Hz
- Baseline irradiance data is measured prior to data collection
- Housekeeping data is stored and monitored for irregularities (temperature, power usage)
- Errors in data are detected and corrected

### 2.0: Communications

- A warning is downlinked when an anomaly in housekeeping data is detected
- Total data volume, including EDAC, does not exceed the data volume of a 9.6 kbps downlink over a 5 minute window.

#### **Project Description**

Baseline Feasibility (Software)



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### SOFTWARE CONOPS

Direction of

Stratospheric Area of Intere

Irradiance data is continuously gathered, redundant EDAC bits are appended. The previous 34.4 seconds of data is stored in a temporary data buffer.

Baseline measurement taken by averaging the data buffer.

Data collection window begins when NanoSAM is aligned with the stratopause.

Sunset detected as photodiode signal crosses below threshhold value. Data buffer is measured against baseline value and copied to long-term memory.

Memory is periodically scanned for errors. Errors are corrected when possible.

Science data and system state data are transmitted to ground reciever.

Sunrise detected as photodiode signal crosses above threshhold value. 34.4 second collection window timer starts.

Collection window timer expires. Data buffer is measured against baseline value and copied to long-term memory.

Sequential action taken by software
 Event

General software function

### **Error Detection and Correction**

**Project Description** 



Baseline Feasibility (Software)

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### Status Summary & Strategy for Conducting Remaining Studies

Axel Haugland

**Project Description** 









1	Data Capture	:	Sufficient Storage Sufficient ADC sampling frequency
2	Communications	:	USB Communication for Ground Testing Data size is small enough to fit in link budget for similar CubeSats
3	SAM-II Equivalent Optics	:	Using proven optic from NanoSAM I SNR can be maintained with this year's electronics design
4	Payload Dimensions	:	Critical Project Elements fit within 0.5U dimensions Mass is less than the NASA recommendation for 0.5U
5	Flight Testing	:	Thermal requirements are feasible with feedback heater Vibrational requirements
6	Cost	:	Current components use less than 20% of the budget As component selections are finalized this will be reevaluated

### Testing NanoSAM I

- Running a Solar Occultation test on the NanoSAM I provides a baseline for our optics design
  - Will use Orion star tracker mount (obtained by the NanoSam I team)
- Some chosen design aspects in the baseline design are the same
  - Verify these aspects are valid early on
- The Customer wants the performance of the previous system to be evaluated
- Receive reference values for our requirements
  - MTF, SNR, Power, etc.

#### NanoSam I Enclosure





#### Orion star tracker mount







### Enclosure

#### **Remaining Studies**

- Pseudo-Random Vibration test based on QB50 random vibration requirements
- Improved Boundary Conditions
  - Multiple cases studied
  - Removal of millihertz modes
- Thermal Model within Solidworks
  - Higher confidence temperature change on optic

#### **Future Work**

- Explore thermal isolation options for the optics bench
- Finalized CAD based on orderable parts
  - Ensure Machinability
- Price quotes on materials/components picked for final design



### Software

#### **Remaining Studies**

- Data collection window duration
  - Sensitivity to orbital parameters
  - Variation with time
- Estimate BCH code computational cost
  - Scales with maximum correctable error size
- Single Event Upset simulation method for testing

#### **Future Work**

- Refine system architecture
  - Finalize module responsibilities
- Hardware integration



# Questions
## References

[1] QB50:System Requirements and Recommendations. Issue 7, Section 1.6 "Thermal Control" and Section 2.2 "Resonance Survey." Published 13 Feb 2015. https://www.qb50.eu/index.php/tech-docs/category/QB50\_Systems\_Requirements\_issue\_76e8e.pdf?download=89:qb50docs

[2] Stafford, George. "Blue Canyon Technologies XB1: Enabling a New Realm of CubeSat Science", slide 6. Accessed Sep 10, 2020. <<u>http://mstl.atl.calpoly.edu/~workshop/archive/2012/Summer/Day%201/1200-Stafford-XB1.pdf</u>>

[3] Anderson, B. & Justus, C. & Batts, G.. (2001). Guidelines for the Selection of Near-Earth Thermal Environment Parameters for Spacecraft Design.

[4] Kovo, Y. (2020, March 12). Thermal Control. Retrieved October 06, 2020, from <u>https://www.nasa.gov/smallsat-institute/sst-soa/thermal-control</u>

[5] Llopis, O, et al. "Photodiode 1/f noise and other types of less known baseband noises in optical telecommunications devices", University de Toulouse, ICNF 2013. Accessed 2 Oct 2020. <u>https://hal.archives-ouvertes.fr/hal-00849396/document</u>

[6] Bell, Michael. "Space Radiation Effects on Electronic Components in Low-Earth Orbit", Published 01 Feb 1999. https://llis.nasa.gov/lesson/824

# **Backup Slides**

## Organization Chart



# Tabulated Specific Objectives

	Level 1 (Solar Tracking Test)	Level 2 (Improved Ground Performance)	Level 3 (Flight Capability Testing)		
Payload Housing	The payload housing contains the integrated electronics board and optics bench inside a 0.5U enclosure.	The payload housing structural interface is compatible with an industry standard bus	The payload housing functions within the operating temperature range of -20C to 60C and its lowest vibrational natural frequency is greater than 90Hz [1].		
Data Capture Software and electronics acquires, digitizes, packetizes, and down-loads raw data from a photodetector to a computer at a rate of at least 50Hz within the mission-specific measurement schedule detailed in the CONOPS		Error checking measures are implemented in the ground software to detect data corruption occurring during transmission	Data is transferred from the payload to an industry standard CubeSat bus communications system[2].		
Electronics & Control	The redesigned electronics board successfully controls and powers all on-board operations and has a footprint compatible with the 0.5U payload enclosure	The redesigned electronics board supports all optical design improvements.	The redesigned electronics board remains within the operating temperature range of -20C to 60C and its lowest vibrational natural frequency is greater than 90Hz [1].		

## **Electronics: Split Board Connection**





An inboard through hole connector will provide more space and reliability than a curved cord connector.

With 16 I/O pins as the regular, we will be able to route all signals required between boards.

Separation of data line and voltage line noise is possible due to the large length.

## **Electronics: Analog Signal Conditioning**



$$f_{BW} = \frac{1}{(2\pi)R_L C_J}, \ t_R = \frac{0.35}{f_{BW}}$$

$$V_{out} = I_s R_f + V_B$$
$$V_B = V_{cc} \left(\frac{R_3}{R_3 + R_2}\right)$$
$$I_s = RP_o$$
$$C_f = 0.5\pi f R_f$$
$$GBP = 2f^2 \pi R_f (C_f + C_J)$$

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## **Electronics: Analog To Digital Converter**



$$LSB = \frac{V_{cc}}{2^n}$$

Other key characteristics:

- Sampling frequency (833 s/sec)
- Accuracy of reference with temperature (—10ppm/°C)
- Offset error (1mV)
- Gain Error (0.01%)
- Supply current (3.5mA)

## **Electronics: Photodiode Saturation**



The photodiode output will saturate when the output voltage approaches the reverse bias voltage on the photodiode.

Our reverse bias voltage should be greater than the maximum output voltage, which is 3.3V.

## **Electronics: Photodiode Temperature Effects**



~3% deviation for 5K change, leads to 0.0027 A/W per K change in responsivity. This is equivalent to a 0.0198 V/K response in our output voltage (with 5121 ohm feedback resistor).

This is 393 bins / K (0.6% error in voltage read out per kelvin).

We must size the feedback resistor for the estimated responsivity at the expected temperature of the photodiode during the data collection window. From our thermal analysis, we must make sure temperature cannot fluctuate in the 34.4 second data capture window.

"Optical Detectors", Wang, Wei-Chih, National Tsing Hua University.

## **Electronics: Controllable Gain Resistor**



Simple, cheap integrated circuits allow for a variable resistor controlled via SPI interface from our microcontroller

Using this as the feedback resistor in the second amplifier stage would allow for controllable gain, although this might not be necessary.

## **Electronics: Thermal Regulation & Measurement**



Resistance of the thermistor changes depending on the temperature, and thus affects the voltage measurement in the typical voltage divider equation.

We amplify the output because the change in resistance is quite small.

## Electronics: Microcontroller & Flash

Serial Programmable Interface (SPI) for the ADC and Flash I/O.

### Nine analog read pins:

- 1-3: Thermistor
- 4-5: Voltage regulator current monitors
- 6: Raw photodiode current
- 7: 3.3V Regulator output
- 8-9: Unused

Additional pins can all be used for digital I/O, and most digital pins can be used for analog write commands.



## Electronics: Required Storage Space

Science Data: (833 s/sec)(34.4 sec)(16 bit)(2 periods) = 916966 bits

- ADC Range: 16 bits
- ADC Sampling Maximum: 833 s/sec
- Data collection window: 34.4 sec
- Number of data collection periods: 2

Housekeeping Data: (8 pins)(12 bit)(10 s/sec)(5670 sec) = 5.44e6 bits

- Number of analog read pins: 8
- Microcontroller ADC Range: 12 bit (can be lowered)
- Microcontroller Sampling: 10 s/sec (can be lowered)
- Data collection window: 1 orbital period = 5670 seconds

Available: 280Mb

Flash 1: 128 Mb, Flash 2: 128 Mb, Teensy Flash: 15.872 Mb, Teensy RAM: 8.192 Mb 49

## Electronics: SPI Bus Connections





https://www.analog.com/en/analog-dialogue/articles/introduction-to-spi-interface.html

## **Electronics SPI Bus Noise Considerations**



# Electronics: Voltage Regulation

A current sense amplifier is used for the load into each regulator.

The 5V regulator is a low noise inverting dual supply regulator, turning the bus voltage into a positive and negative 5V for the analog supply.

The 3.3V regulator is a bipolar step down regulator, turning the bus voltage into a 3.3V signal for digital electronics.



# Electronics: Recommended Voltage Regulator Layouts

Digital Voltage Regulator



#### Analog Voltage Regulator



## **Electronics: Risk Management**

## Primary

- 1) Analog to Digital Converter
- 2) Flash Storage
- 3) 5V Voltage Regulator
- 4) 3.3V Voltage Regulator

## Backup

- 1) Microcontroller 12 bit on board ADC
- 2) Secondary flash storage could hold all data required. Microcontroller flash storage could hold science data only.
- No backup planned for voltage regulators. If 3.3V regulator fails, microcontroller will lose power. If 5V regulator fails, photodiode data will be unreadable.

## **Electronics: Extended Bill of Materials**

Part	Value	Device	Package Si	ze (mm^2) DK Part #	Cost	Total Size (mm^2)	Total Cost (\$)	# Components
C1	100pF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37	3969.956744	453.24	131
C2	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C3	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C4	4.7uF - 25V	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C5	1uF	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C6	4.7uF	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C7	.01uF	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C8	.01uF	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C9	10uF	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C10	10uF	4.7UF-1206-16V-(+80/-20%)	1206	4.645152 311-2011-2-ND	0.29			
C11	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C12	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C13	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C14	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C15	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C16	1uF	4.7UF0603	603	1.16 478-11510-2-ND	0.37			
C17	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C18	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C19	.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C20	.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C21	0.1uF	0.1UF-0603-25V-5%	603	1.16 478-11510-2-ND	0.37			
C22	0.1uF	0 1UE-0603-25V-5%	603	1 16 478-11510-2-ND	0.37			

## **Electronics: Power Budget Calculations**

Photodiode: (5V)(5mA) = 0.025 W [Maximum] Amplifier: (3.3V + 3.3V)(12nA + 3mA) = 0.0198 W [Maximum] Feedback Resistor: (5V)<sup>2</sup> / (5121.04 ohms) = 0.004882 W [Maximum] ADC: (3.3V)(5 mA) = 0.0165 W [Maximum] Microcontroller: (3.3V)(100 mA) = 0.33 W [Expected] Flash Storage: (3.3V)(8 mA)(2 modules) = 0.0528 W [Expected] Thermistors:  $(3.3V)(170 \text{ uA})(3) + (3)(3.3V)^2 / (20e3 \text{ ohms}) = 0.003317 \text{ W}$ [Expected] Current Sense Amps: (12V)(150 uA) = 0.0036 W [Expected] 3.3V Regulator: (12-3.3V)(100 mA) = 0.87 W [Maximum] 5V Regulator: (12-5V)(50mA) = 0.35 W [Maximum]

## Total: 1.656 W

 $\mathbf{P} = \mathbf{IV}$ 

## Electronics: SNR Detailed Calculations

All noises are converted to an equivalent voltage, and compared to the expected voltage signal to the ADC. For a worst case scenario, we assuming the typical signal will be 50% of our dynamic range, or 1.65V.

$$\begin{split} V_{shot} &= R_f * \sqrt{2q(I_s + I_d)f} \\ V_{johnson} &= R_f * \sqrt{\frac{4k_BTf}{R_{shunt}}} \\ V_{dark} &= R_f * Idark \\ V_{lowpass} &= \frac{28nV}{\sqrt{f}} \\ V_{regulator} &= 0.1mV \\ V_{quantization} &= \frac{LSB}{\sqrt{12}} \\ SNR &= \frac{0.5 * I_s R_f}{\sqrt{V_{quantization}^2 + 2V_{regulator}^2 + V_{lowpass}^2 + V_{dark}^2 + V_{johnson}^2 + V_{shot}^2} \end{split}$$

## Electronics: Resistor Heater

3.3V Linear Regulator can supply the needed 1.65 A. This increases the power requirement of the system a lot. We can directly use the 12V input to save power usage, controlling the input with a TVS diode.

MOSFET provides a very low voltage drop, but can be accounted for in resistor sizing.

Microcontroller can supply the activation signal with a PWM output

-20-50°C P=IV - I. 5.46 00 tem orge I = 1.65455 A N-chamel Enhonament RH Prover = IRDS. Ros floor dotusheet for DOUES chown maslet Vin = 3.3V (~3,2 ish may're im The HI murach Design constraints : Q= 1% = 3.3% Ru Vin, max = 330, 10m / max Vec = 3.3V, 3.5 A mox Preg = 5.46 w (acus RH) RH = V2/p - 3.3V/5.46 w RH = 1. 9945 IL ERRORS Ling La when der: VGD = Vin < Vin Use ~ 2-10mV (HE signal is ~3.2-3.3V) ID 20 (extremy luw) ROS 20 0. 1 12 [dalashed] "Lo when an : VGD >> Utr Vo> = I0800 = 0.16V Incorporating 1000 = RH. (Vec-Vos)2 (3.3-16)2 RH 2 1.80579 JL

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## Relevant Requirements

### **1.0: SAM-II Equivalent Optics**

- The optics system shall capture light at a center wavelength of 1.03  $\mu m$
- The optical design shall have a vertical resolution of 1 km
- The FOV shall be 1.3 arcminutes to achieve a vertical resolution of 1 km
- The imager shall have a MTF of 0.74 in order to meet the resolution and contrast of the SAM-II system
- The system shall demonstrate solar tracking accuracy of 1 arc-min/mRad or finer during ground testing

### 4.0: Payload Dimensions

- Payload size must be less than 0.5U
- Payload mass less than 0.615 kg

- Herschelian Telescope with 25.4 mm diameter OAP mirror
- 20 x 5 mm aperture
- Pinhole of 15 µm diameter
- Focal length of 54.45 mm
- Bandpass and longpass filters
- Fits within the dimensions described in the enclosure section

## Feasibility of NanoSAM I Design

The optical system designed by NanoSAM I is composed of a Herschelian telescope using an Off-Axis Parabolic (OAP) mirror. This system also had a rectangular aperture of dimensions 20 x 5 mm and a pinhole diameter of 15  $\mu$ m. Filters used to isolate light with a CWL of 1.03  $\mu$ m. Optical bench designed to hold optical system





Figure 2.3 Map showing the design types which are commonly used for various combinations of aperture and field of view.

## F-Number

+ 54.45mm F/# : 30 0.015 mm

The system has a pinhole diameter of 0.015 mm and a focal length of 54.45 mm, therefore it has a large F-number of 3630

## Field of View (FOV)

An estimate for the field of view can be found using the equation of 57.3 divided by the F-number which yields a value of .9471 arcminutes. This is good as the value of 1.3 arcminutes is the absolute maximum possible value for the vertical resolution requirement as any larger a FOV, will cause the resolution to be larger than 1 km. This value good to design for as it provides a margin for the system to actually achieve a value much closer to 1.3 arcminutes, as the true value can be calculated using convolutions.

## Modular Transfer Function (MTF)

The graph on the right shows the NS1 calculated MTF vs. SAM-II MTF. It can be seen that the MTF of NS1 was found to be larger than the MTF of SAM-II



## OAP Feasibility

The study conducted by the NS1 team concluded that the WFE due to aberrations caused by deformations on the surface of the OAP was within acceptable levels due to a minimal decrease in MTF





# Alignment Procedure

- 1. Align OAP Mirror optical axis parallel to Interferometer (INT) Beam axis using tilt micrometers
  - a. Photodiode Block mounted to 3-axis stage
  - b. Use INT Return Half Sphere to align OAP-INT Beam
- 2. Aligning Pinhole Disk on Focus Point
  - a. Chrome Half Sphere (HS) centered over pinhole
  - b. Interferometer measurements are taken to approximately locate the OAP's focus point relative to HS
  - c. Shims are inserted and the Photodiode block is firmly placed into the optical bench mounting point
  - d. Interferometer measurements are taken to measure focus point offsets
  - e. Steps (c) and (d) are repeated until the pinhole is centered on the focus point within required tolerances



# Alignment (cont.)

The main testing to be done on the optical system is alignment testing, which is performed using an interferometer





Zernike Coefficents Versus in-plane Tilt Angle

## Indoor Testing – Irradiance Verification

Indoor Testing:

- Use optical system to measure irradiance in a dark room with no lights on
- Place lightbulb with known irradiance in front of optical system
- Measure irradiance using optical system - ensure that photodiode is both receiving and accurately measuring light



# Outdoor Testing – Langley Plot

**Outdoor Testing:** 

- Take solar irradiance measurements outdoor periodically over a set period of time (Ex: every minute from 10am to 3pm)
- Compare solar irradiance measured to that measured by the CU boulder skywatch observatory
- Create Langley plot based off of collected data, and extrapolate it to find the solar irradiance at zero airmass and compare to expected value
- Zero airmass solar irradiance extrapolated from Langley Plot is used to estimate photodiode saturation (target of ~80%)



Langley Plot Simulated by NanoSAM I 69

## Optics Summary Slide

Overall, the optical system designed will meet or exceed SAM II performance

- Using the chosen filters, a center wavelength of 1.03 micrometers will be captured
- With a pinhole of 15 micrometers, the field of view of 1.3 arcminutes will be met, which will allow for a vertical resolution of 1 km.
- NanoSAM I was modeled to have an MTF of about .88 at the given frequency, exceeding the MTF requirement of .74
- The solar tracker purchased by NanoSAM I meets the 1 arc-min/mRad requirement tracking accuracy requirement

## Enclosure – Thermal Model

#### Key Equations:

$$\dot{Q}_{out} = 2\sigma\epsilon T^4_{sys,eq} (2A_s + A_p)$$

 $\dot{Q}_{in,sun} = \alpha A F_{s,sun} A_s G_s$ 

$$\dot{Q}_{in,A} = \alpha A_s G_{E,A} [A_s (AF_s + AF_{-s}) + A_h (AF_h + AF_{-h}) + A_p (AF_p + AF_{-p})]$$

$$\dot{Q}_{in,IR} = \epsilon A_s G_{E,IR} [A_s (AF_s + AF_{-s}) + A_h (AF_h + AF_{-h}) + A_p (AF_p + AF_{-p})]$$

$$T_{sys,eq} = \left[\frac{\dot{Q}_{in,sun} + \dot{Q}_{in,A} + \dot{Q}_{in,IR} + P_i}{2\sigma\epsilon(2A_s + A_p)}\right]^{(1/4)}$$
$$\dot{Q}_{tot} = \dot{Q}_{in,A} + \dot{Q}_{in,IR} + \dot{Q}_{in,sun} + P_i - \dot{Q}_{out}$$
$$\dot{Q}_{in,A} = \dot{Q}_{in,A} + \dot{Q}_{in,IR} + \dot{Q}_{in,sun} + P_i - \dot{Q}_{out}$$

$$T_i = \frac{Q_{tot}\Delta t}{mc_p} + T_{i-1}$$

#### **Symbol Definition**

$\sigma$	- Boltzmann Constant [W/m²K⁴]
E	- IR emissivity of the external surfaces of the CubeSat
Gs	- Heat flux from sun [W/m²]
T <sub>sys.eq</sub>	- System temperature equilibrium [K]
A	- Area associated with <b>ith</b> side sides [m <sup>2</sup> ]
а	- Solar absorptivity of the external surfaces of the CubeSat
AF <sub>i.sun</sub>	- Sun area factor of <b>s</b> surface
G <sub>E.A</sub>	<ul> <li>Albedo radiation flux density from Earth [W/m<sup>2</sup>]</li> </ul>
G <sub>EIR</sub>	<ul> <li>Longwave radiation flux density from Earth [W/m<sup>2</sup>]</li> </ul>
AF	- Albedo area factor of i surface
$Q_{\text{in,sun}}$	- Power into the system from the Sun [W]
$Q_{\text{in,A}}$	- Power into the system from albedo [W]
$Q_{in IR}$	- Power into the system from long-wave radiation [W]
Pi	- Internally dissipated power [W]
$Q_{\text{out}}$	- Power out of the system [W]
Q <sub>tot</sub>	- Net power into/out of the system [W]
Δt	- Transient timestep [s]
m	- Mass of aluminum external structure [kg]
C <sub>n</sub>	- Specific heat of the external structure [kJ/kg-K]
T <sub>i</sub>	- Temperature at ith timestep [K]

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## **Enclosure: Thermal Area Factor Analysis**

Area factor determines the effective area exposed to radiation and results in the following equations for each side:

 $AF_{h} = -sin(\theta_{h})$  $AF_{s} = -cos(\theta)cos(\theta_{h})$  $AF_{p} = -sin(\theta)cos(\theta_{h})$ 

 $AF_{-h} = sin(\theta_h)$ 

- $AF_{-s} = \cos(\theta)\cos(\theta_h)$
- $AF_{-p} = sin(\theta)cos(\theta_h)$

 $\theta_{h}$  - angle derived from the beta angle of the orbit (position above/below the solar plane)  $\theta$  - angle derived from satellite position in orbit (offset from argument of latitude)


#### Minimum Power For Heater (SG121FD White Paint)





### Thermal Coating Alternative: Black Dyed Anodize

 $\alpha$  = 0.54,  $\epsilon$  = 0.75



# Thermal Coating Alternative: Black Dyed Anodize (Cont.)



### Temperature Differentials

#### Largest Temperature Differentials During Data Collection (w/ Heater) :

CASE	ΔΤ (Κ)	Time (min)	ΔT/t (K/min)
Cold (sunrise)	1.076	5.730	0.188
Cold (sunset)	0.161	5.730	0.028
Hot (sunrise)	1.442	4.668	0.309
Hot (sunset)	0.124	2.943	0.042

Baseline Feasibility (Enclosure)

**Status Summary** 

# Enclosure – Frequency Study



<text>

0.0017965Hz Mode (Auto Interface)

384.96Hz Mode (Auto Interface)

# Enclosure – Frequency Analysis

Any metal parts (Structural Ribs, Optics Base) are Al-6061-T4 (SS) within Solidworks. These are modeled with tetrahedral linear elastic elements.



The **PCBs are FR-4**, a common PCB material. These are modeled with tetrahedral **linear elastic** elements.



The LORD Nut/Nut elastomers (commercially available thermal/vibrational isolator) are modeled as silicone rubber. These are modeled with tetrahedral hyperelastic elements. This model was selected as silicone rubber is decidedly not linear elastic. The methodology for selecting the coefficients required is discussed in <u>"A</u> comparative study of several material models for prediction of hyperelastic properties: Application to Silicone-Rubber and Soft Tissues" by Pedro et al.



### Enclosure – Cost Breakdown

#### New components to purchase:

Widely Available from McMaster Carr.

- Structural Aluminum
  - Al 6061-T6 (high tolerance): 4" x 12" x 6mm (\$67.18)
- Connecting Screws
  - Screw type 2-56 5/16" \$6.50
  - Screw type 2-56 5/8" \$4.38
  - Screw type 0-80 3/16" \$8.09
- Elastomers
  - Parker LORD Nut/Nut (TBD)
- Housing Siding
  - Al 6061-T6 (high tolerance):
    6" x 12" x ⅓" (\$84.00)
  - Should consider other materials to reduce cost and mass

#### Components not included in cost:

- Optical Bench
- Filter Unit
- Electronics Boards

#### Total:

\$67.18 + \$6.50 + \$4.38+ \$8.09 + \$84.00 =

 $170.15 \rightarrow x2.5 = 425$ 

### Software – previous system

